R&S[®]RTP-K110 HDMI Compliance Test User Manual







Make ideas real

This document describes the HDMI compliance test procedures of the following option:

• R&S[®]RTP-K110 (1802.9467.02)

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Throughout this manual, products from Rohde & Schwarz are indicated without the ® symbol , e.g. R&S[®]ScopeSuite is indicated as R&S ScopeSuite.

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 3.6 4 4.1 4.2.1 4.2.2 4.3 4.3.1 4.3.2 4.3.3 4.3.4 4.3.5 5 	Test configuration for HDMI	 17 20 20 20 22 24 24 25 26 27 29 31
 3.6 4 4.1 4.2 4.2.1 4.2.2 4.3 4.3.1 4.3.2 4.3.3 4.3.4 4.3.5 5 5.1 	Test configuration for HDMI	 17 20 20 20 22 24 25 26 27 29 31 31

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1 R&S ScopeSuite overview

The R&S ScopeSuite software is used with R&S RTP oscilloscopes. It can be installed on a test computer or directly on the oscilloscope. For system requirements, refer to the Release Notes.



The R&S ScopeSuite main panel has several areas:

- "Settings": connection settings to oscilloscope and other instruments also default report settings
- "Compliance Tests": selection of the compliance test
- "Demo": accesses demo test cases that can be used for trying out the software without having a connection to an oscilloscope
- shift sideways to change the transparency of the dialog box
- "Help": opens the help file, containing information about the R&S ScopeSuite configuration
- "About": gives information about the R&S ScopeSuite software
- "Tile View": allows a personalization of the compliance test selection You can configure which tests are visible in the compliance test section and which are hidden, so that only the ones you use are displayed.
- To hide a test from the "Compliance Tests" view, do one of the following:

Right-click on the compliance test that you want to hide.
 The icon of the test changes, see Figure 1-1. Now with a left click you can hide the test.



Figure 1-1: Unpin icon

b) Click on "Title View" to show a list of the available test cases. By clicking a test case in the show list, you can pin/unpin it from the main panel.

2 Preparing the measurements

2.1 Test equipment

For HDMI compliance tests, the following test equipment is needed:

- R&S RTP with 4 channels and minimum 8 GHz bandwidth
- R&S RTP-K110 HDMI compliance test option (required option, installed on the R&S RTP)
- 2 modular probes, R&S ZM160 with 9 GHz bandwidth.
- 2 modular probe tips R&S RT-ZMA40
- HDMI Termination Module and an HDMI TPA adapter
- EDID Emulator
- R&S RTP-K140/-K141 High Speed Serial Triggering 8G/16G is required for:
 - HDMI 2.1 TMDS: Differential Voltage Mask Tests
 - Jitter Mask Tests
 - Inter-Pair Skew
- The free-of-charge R&S ScopeSuite software, which can be installed on a computer or directly on the R&S RTP.

2.2 Installing software and license

The preparation steps are performed only once for each computer and instrument that are used for testing.



Uninstall older versions of the R&S ScopeSuite

If an older version of the R&S ScopeSuite is installed, make sure to uninstall the old version before you install the new one. You can find the version number of the current installation in "Help" menu > "About". To uninstall the R&S ScopeSuite, use the Windows " Control Panel" > "Programs".

For best operation results, we recommend that the installed firmware versions of the R&S ScopeSuite and the oscilloscope are the same.

To install the R&S ScopeSuite

- Download the latest R&S ScopeSuite software from the "Software" section on the Rohde & Schwarz R&S RTP website: www.rohde-schwarz.com/product/rtp.html
- 2. Install the R&S ScopeSuite software:
 - On the computer that is used for testing, or

On the R&S RTP.

For system requirements, refer to the Release Notes.

To install the license key on the R&S RTP

When you got the license key of the compliance test option, enable it on the oscilloscope using [Setup] > "SW Options".

For a detailed description, refer to the R&S RTP user manual, chapter "Installing Options", or to the online help on the instrument.

2.3 Setting up the network

If the R&S ScopeSuite software runs on a test computer, the computer and the testing oscilloscope require a LAN connection.

There are two ways of connection:

- LAN (local area network): It is recommended that you connect to a LAN with DHCP server. This server uses the Dynamic Host Configuration Protocol (DHCP) to assign all address information automatically.
 If no DHCP server is available, assign fixed IP addresses to all devices.
- Direct connection of the instruments and the computer or connection to a switch using LAN cables: Assign fixed IP addresses to the computer and the instruments and reboot all devices.

To set up and test the LAN connection

- 1. Connect the computer and the instruments to the same LAN.
- 2. Start all devices.
- 3. If no DHCP server is available, assign fixed IP addresses to all devices.
- 4. Ping the instruments to make sure that the connection is established.
- 5. If VISA is installed, check if VISA can access the instruments.
 - a) Start VISA on the test computer.
 - b) Validate the VISA address string of each device.

See also:

Chapter 2.5, "Connecting the R&S RTP", on page 9

2.4 Starting the R&S ScopeSuite

To start the R&S ScopeSuite on the test computer or on the oscilloscope:

Double-click the R&S ScopeSuite program icon.

To start the R&S ScopeSuite on the instrument, in the R&S RTP firmware:

▶ In the "Apps" dialog, open the "Compliance" tab.

2.5 Connecting the R&S RTP

If the R&S ScopeSuite is installed directly on the instrument, the software detects the R&S RTP firmware automatically, and the "Oscilloscope" button is not available in the R&S ScopeSuite.

If the R&S ScopeSuite software runs on a test computer, the computer and the testing oscilloscope require a LAN connection, see Chapter 2.3, "Setting up the network", on page 8. The R&S ScopeSuite software needs the IP address of the oscilloscope to establish connection.

- 1. Start the R&S RTP.
- 2. Start the R&S ScopeSuite software.
- 3. Click "Settings" > "Oscilloscope".

R&S ScopeSuite							_ 🗆 ×
					Tile V	View 🚺 About	P Help
Settings	Compliance Tes	ts					
Oscilloscope	Ethernet	日 1000BASE-T1	USB3.2-RX	НДМІ	eMMC		
Instruments	모 2.5/5/10G Ethernet 2.5/5/10G	中 立 2.5/5/106 2.5/5/106 MGBASE-T1	DDR3	DisplayPort	Demo		
Report	10BASE-T1	PCIe	DDR4	MIPI D-PHY			
		USB	DDR5	MIPI C-PHY			
Welcome to complian	ce tests selection scr	een.					

- Enter the IP address of the oscilloscope. To obtain the IP address: press the Rohde & Schwarz logo at the top-right corner of the oscilloscope's display.
- 5. Click "Get Instrument Information".

The computer connects with the instrument and gets the instrument data.

Report configuration

RSScopeSuite			_ 🗆 ×
🕒 Back Oscilloscope	e Settings	About	P Help
Oscilloscope			
IP address:	: 10.113.10.30		
	Get Instrument Information		
Device:	: RTO		
Serial Number:	: 400132		
Firmware Version:	: 2.60.2.7		
Restore Settings On Exit:	: Never Ask Always		
Connect software to your RTO.			

If the connection fails, an error message is shown.

2.6 Report configuration

In the "Report Configuration" menu, you can select the format of the report and the details to be included in the report. You can also select an icon that is displayed in the upper left corner of the report.

Also, you can enter common information on the test that is written in the "General Information" section of the test report.

Report configuration

R&S ScopeSuite						•	_ 🗆 🗙
G Back Report Settings						() About	P Help
Content	Format		lcon				
Display Summary 🗸		PDF		Re C	Change		
Display Detail 🗸		O Word Document		X9			
Display Properties 🗸							
Display Screenshots 🗸		Display SVG Chart ✔					
Reports Directory							
Directory				🗘 Change	😭 Open		
User Input							
Device Under Test (DUT)							
User							
Site							
Temperature							
Comments							
Configure default settings for new se	ession						

3 Performing tests

3.1 Starting a test session

R&S ScopeSuite			_ 🗆 ×
G Back Compliand	ce Tests HDMI	1 About	🕐 Help
Select Type			
 2.1 T 	MDS 🔵 1.4		
Session Name	Last Accessed	Comment	
2.1 TMDS_20210412_1625	4/13/2021 6:53:36 PM	Type in your comment.	
🕂 Add 🖆 Open	💼 Remove 🖳 Rem	name 📮 Comment 🔥 Show Report	
Add new or open existing se	ession to run.		

After you open a compliance test, the "Session Selection" dialog appears. In this dialog, you can create new sessions, open or view existing report.

The following functions are available for handling test sessions:

Function	Description
"Add"	Adds a new session
"Open"	Opens the selected session
"Remove"	Removes the selected session
"Rename"	Changes the "Session Name"
"Comment"	Adds a comment
"Show report"	Generates a report for the selected session

To add a test session

- 1. In the R&S ScopeSuite window, select the compliance test.
- 2. In the "Session Selection" dialog press "Add".
- 3. If necessary change the "Session Name"

To open a test session

- 1. In the R&S ScopeSuite window, select the compliance test.
- 2. In the "Session Selection" dialog, select the session you want to open and double click on it.

Alternatively, select the session and press "Open".

To show a report for a test session

- 1. In the R&S ScopeSuite window, select the compliance test.
- 2. In the "Session Selection" dialog, select the session you want the report for and press "Show report".

3.2 Configuring the test

- 1. In the R&S ScopeSuite window, select the compliance test to be performed:
 - "HDMI"
- 2. Open a test session, see Chapter 3.1, "Starting a test session", on page 12.
- 3. Adjust the "Properties" settings for the test cases you want to perform.
- 4. Click "Limit Manager" and edit the limit criteria, see Chapter 3.2.1.1, "Limit manager", on page 15.
- If you want to use special report settings the "Report Config" tab to define the format and contents of the report. Otherwise the settings defined in "RSScopeSuite" > "Settings" > "Report" are used. See Chapter 2.6, "Report configuration", on page 10.
- Click "Test Checked"/"Test Single" and proceed as described in the relevant test case chapter.

3.2.1 General test settings

R&S Scope	Suite					_		_ 🗆 ×
G Back	Session 2.1 TMDS_20240610_164123					Reg Show Report	About	P Help
	All	Properties	Limit Manag	er Results	Report Conf	lig		
	A HDMI 2.1 TMDS	Channels						<u></u>
	▲ Clk				Skew			
	▼ All Clock Tests		Diff	Jn Ch1 ▼	0 1	DS		
	▲ Data		1st Diff	Ch1 ▼	0 1	DS		
	 Single-Ended Tests 		2nd Diff	∫ Ch3 ▼	0 p	ps		
	 Differential Timing Tests 	Sir	gleEnded Pos	Jag Ch1 ▼	0 k	ps		
	 Differential Voltage Mask Tests 	Sin	gleEnded Neg	°L_ Ch3 ▼	0 k	ps		
					Retrieve Skey	w		
	Inter-Pair Skew (HF 1-3)							
		EDID Em	ulator					
				● Wilder ◯) GRL 🔿 Allio	on		
		Data Lan	e					
				-				
				0 +				
		Inter Pair						
				D0D1 V				
		Test Setu	р					
			Vcc	3.3 V				
		Pi	el Clock Rate	594 M	IHz			
			0(1)	250				
		N	umper Of bits	250 K				
			Serial Pattern	001010101100	010101011			
		Export W	aveforms					
			Enable					
⊡≱ Test 0	Thecked	Offline E	ecution					
Ready to ru	n.							

Each session dialog is divided into several sections:

 "Properties": shows the settings that can be made for the test case selected on the left side of the dialog. You can differentiate between the "All" and the sub test properties

In the "All" > "Properties" tab you can configure the settings for all test cases in the current session. Once you change and save a setting in this tab, the changes will be done for all test in the sessions. At the same time, there will be a special marking for the functions that have different settings for different sub tests.

- "Limit Manager": sets the measurement limits that are used for compliance testing, see Chapter 3.2.1.1, "Limit manager", on page 15.
- "Results": shows an overview of the available test results for this session.
- "Instruments": defines instruments settings for connecting to external devices, that are specific for this test session.
 When a session is first created the global settings ("RSScopeSuite" > "Settings" > "Instruments") are copied to the session. This "Instruments" tab can be used to change those copied defaults.
- "Report Config": defines the format and contents of the report for this session. When a session is first created the global settings ("RSScopeSuite" > "Settings" > "Report") are copied to the session. This "Report Config" tab can be used to change those copied defaults.

• "Test Checked"/ "Test Single": starts the selected test group.

3.2.1.1 Limit manager

The "Limit Manager" shows the measurement limits that are used for compliance testing.

Each limit comprises the comparison criterion, the unit, the limit value A, and a second limit value B if the criterion requires two limits.

You can set the values to defaults, change the values in the table, export the table in xml format, or import xml files with limit settings.

You can also return the values to the original limits with "Reset to default".

▲ All	Properties	Limit Manager Results	s Repor	t Config				
HDMI 2.1 TMDS	Maaguran	vont	Calmain		Unit		D	
▲ Clk	Clock Dut	v Ovclo Maximum	Criteria		%	A 60	в	
▼ All Clock Tests	Clock Dut	v Cycle Minimum	x - A +		96	40		
▲ Data	Clock Eul	Time	x>-A +		70	7 5E 11		
 Single-Ended Tests 	Clock Intra	-Pair Skow	x>=A +		3	15		
 Differential Timing Tests 	Clock litte	r all skew	XX=A +		70 96	30		
 Differential Voltage Mask Tests 	Clock Pate		A curch =		/0 U-7	9.55±07	1 55+09	
 Jitter Mask Tests 	Clock Rise	Time	AKXKD +		112	7 55 11	1.52+00	
Inter-Pair Skew (HF 1-3)	Clock VI N	lagativo	A CONCOR	-	v	2.3	3.1	
	Clock VI P	ositivo	A<-x<-D	-	v	2.5	3.1	
	Clock Vh I	Vogativo	A<-x<-D	-	v	2.5	3.31	
	Clock Vh I	Positivo	A<-x<-D	-	v	2.9	3.31	
	Clock Vew	ing Negative	A<	-	v	0.2	0.6	
	Clock Vsw	ing Resitive	A<=x<=D	-	v	0.2	0.0	
	Clock Vsw	ing TD1	A<=x<=D	-	v	0.4	1.2	
	Data Fall J	ling ir i	A<=X<=D	*	v	4 255 11	1.2	
	Data Inter	Dair Chaw	x>=A +		5 0/	4.231-11		
	Data Inter	Pair Skow	X<=A +		20 0/	15		
	Data Mad	-rail Skew	X<=A +		70	15		
	Data Mavi	num Differential Veltage	ignore +		N	0.79		
	Data Mini	mum Differential Voltage	X<=A +		V	0.70		
	Data Nini	Time	x>=A +		v c	4 25E_11		
	Data Kise	Time	x>=A *		S	4.250-11		
	Data VI N	egative	x>=A *		V	2.5		
	Data Veui	na Nagativa	X<=A +		V	2.5		
	Data Veui	ng Negative	x>=A *		v	0.6		
	Data VSWI	ng rositive	x<=A ₩		v	0.0		

Check and adjust the measurement limits.

3.3 Initiating the test

To perform compliance tests, the device under test is connected to the test board in a test-specific way. Using a probe, the test board is connected with the R&S RTP. The probe connections are test-specific. The R&S ScopeSuite guides you step-by-step through the connection setup and the test sequence.

- 1. Set the test setup on a nonconductive, static-approved work surface.
- 2. In the R&S ScopeSuite window, select the compliance test.
- 3. Open a test session, see Chapter 3.1, "Starting a test session", on page 12.
- Check the test configuration settings and adjust, if necessary. See: Chapter 3.2, "Configuring the test", on page 13.
- Click "Test Checked" for starting all checked test cases or "Test Single" for starting only the selected test case.

The R&S ScopeSuite test wizard explains the following individual setup steps. A detailed test description can be found in the following chapters:

- Chapter 4, "HDMI 1.4 tests", on page 20
- Chapter 5, "HDMI 2.1 TMDS tests", on page 31

3.4 Getting test results

If you resume an existing session, new measurements are appended to the report, new diagrams and waveform files are added to the session folder. Existing files are not deleted or replaced. Sessions data remain until you delete them in the "Results" tab of the session.

The report format can be defined in "RSScopeSuite" > "Settings" > "Report" for all compliance tests (see also Chapter 2.6, "Report configuration", on page 10). If you want to use special report settings for a session, you can define the format and contents of the report in the "Report Config" tab of the session.

All test results are listed in the "Results" tab. Reports can be provided in PDF, MSWord, or HTML format. To view and print PDF reports, you need a PDF viewer, for example, the Acrobat Reader.

The test report file can be created at the end of the test, or later in the "Session Selection" dialog.

To show a test report

- 1. In the R&S ScopeSuite window, select the compliance test to be performed.
- 2. Select the session name in the "Session Selection" dialog and click "Show report".

The report opens in a separate application window, depending on the file format. You can check the test results and print the report.

To delete the results, diagrams and waveform files of a session

- 1. In the "Session Selection" dialog select the session and open it.
- 2. In the "Results" tab, select the result to be deleted.
- 3. Click "Remove".

3.5 Starting HDMI tests

Before you run the test, complete the following actions:

- LAN connection of the oscilloscope and the computer running the R&S Scope-Suite, see Chapter 2.5, "Connecting the R&S RTP", on page 9
- 1. Select "HDMI" in the R&S ScopeSuite start window.
- In the "Session Selection" dialog, set the "Select Type" standard. The following "Types" are available:
 - "2.1 TDMS":
 - "1.4":
- 3. Add a new test session.
- Open the session. For details, see Chapter 3.1, "Starting a test session", on page 12.
- 5. Check the test configuration settings. Adjust, if necessary. See:
 - Chapter 3.6, "Test configuration for HDMI", on page 17
 - Chapter 3.2.1.1, "Limit manager", on page 15
- Select/check the test cases you want to run and click "Test Single"/"Test checked".
- 7. A step-by step guide explains the following individual setup steps. When you have finished all steps of the step-by-step guide, the compliance test runs automatically.

3.6 Test configuration for HDMI

The test configuration consists of some test-specific configuration settings. The values for the settings in this tab depend on the selected version of the standard.

Test configuration for HDMI

R&S ScopeSuite	• _ ¤ >
G Back Session 2.1 TMDS_20240610_164123	R Show Report 1 About 1 Help
All	Properties Limit Manager Results Report Config
HDMI 2.1 TMDS	Channels
Clk	Skew
All Clock Tests	Diff 🗸 Ch1 👻 0 ps
Data	1st Diff Ch1 v 0 ps
Single-Ended Tests	2nd Diff 🕂 Ch3 👻 0 ps
Differential Timing Tests	SingleEnded Pos 🎝 🙀 Ch1 👻 0 ps
Differential Voltage Mask Tests	SingleEnded Neg 🚰 Ch3 🔻 0 ps
Jitter Mask Tests	Retrieve Skew
Inter-Pair Skew (HF 1-3)	
	EDID Emulator
	Wilder GRL Allion
	Data Lane
	0 *
	Later Drin
	Inter Pair
	D0D1 💌
	Test Setup
	Vcc 3.3 V
	Pixel Clock Rate 594 MHz
	Number Of bits 250 K
	Serial Pattern 00101010101010101
	Export Waveforms
	Enable
☑ Test Checked	Offline Execution
Ready to run.	

Figure 3-1: Configuration for HDMI compliance tests

Channels

In the "Channels" section you can select the channel of the probes used for the test setup and set the skew for each channel.

The skew compensates signal propagation differences between channels caused by the different length of cables, probes, and other sources.

You can set a fixed value or retrieve the skew value from the oscilloscope.

EDID Emulator

Selects the EDID Emulator that is used for the measurement. Supported are "Wilder", "GRL" and "Allion".

Data Lane

Selects the data lane that is tested.

Inter Pair

Selects the used inter-pair skew. You can select "D0/D1", "D1/D2", "D0/D2".

 V_{cc}

Voltage of the common collector.

Pixel Clock Rate

Sets the pixel clock rate. The pixel clock rate is a set of predefined values depending on the DUT settings.

For available pixel clock rates, refer to the HDMI compliance test specification.

Number of Bits

Sets the number of bits.

Serial pattern

Sets a serial pattern for the "Inter-Pair Skew" tests.

Export Waveforms

Enables you to export a waveform. You can later load the waveforms to run the tests in the offline mode, see Offline Execution.

You can define an export directory, or use the default one:

```
MyDocuments\Rohde-Schwarz\RSScopeSuite\<Version>\Waveforms\
<ComplianceTest>\<SubTest>\<Speed>\<SessionName>
```

For example:

MyDocuments\Rohde-Schwarz\RSScopeSuite\5.00.0\Waveforms\HDMI\ 2.1 TMDS\HDMI 21 20210114 144116

Offline Execution

If enabled, allows you to use exported waveforms as a source for the execution of the compliance test.

You can select one waveform for each needed signal.

4 HDMI 1.4 tests

4.1 Test equipment

The following equipment is needed for performing HDMI 1.4 compliance tests.

Item	Description, model	Quantity
Rohde & Schwarz oscilloscope	R&S RTP with 4 channels and minimum bandwidth 8 GHz	1
Modular probes	R&S ZM160	2
Modular probe tips	R&S RT-ZMA40	2
HDMI TPA adapter	-	1
HDMI termination module	-	1
EDID emulator	-	1
DUT	HDMI 1.4 device that supports the selected type	1
Option R&S RTP-K140/-K141	Required for the following tests:	1
	Jitter Mask Tests	
	Inter-Pair Skew (7-6)	

4.2 Clock tests

4.2.1 All clock tests

4.2.1.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "1.4" > "Clock" > "All Clock Tests".

Clock tests

R&S ScopeSuite	×
Back Session 1.4_20240610_165816	🖹 Show Report 🚺 About 🕐 Help
All	Properties Limit Manager Results Report Config
HDMI 1.4b	Channels
Clock	
All Clock Tests	Skew
Vl + (7-2)	SingleEnded Pos
VI - (7-2)	SingleEnded Neg Ch3 🔻 0 ps
Rise Time (7-4)	Retrieve Skew
Fall Time (7-4)	EDID Emulator
Intra Pair Skew (7-7)	
Duty Cycle Minimum (7-8)	Wilder O GRL O Allion
Duty Cycle Maximum (7-8)	Test Setup
Jitter (7-9)	
Voltage Off Tests	Vcc 3.3 V
Voff + (7-3)	Pixel Clock Rate 297 MHz
Voff - (7-3)	Export Waveforms
Data	
	LINGUE
	Offline Execution
	Enable
Test Checked	
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

4.2.1.2 Measurements

The measurements in the following chapter verify, if the measured values are within the limits defined in HDMI CTSv1.4b.

The "All Clock Tests" include the following measurements:

VI + (7-2)

The VL Clock+ test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits. The test is passed, if the maximum VI+ is below 2.9 V.

VI - (7-2)

The VL Clock-test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits.

Rise Time/Fall Time

The test verifies that the rise times on the TMDS differential signals are within the limits:

Table 4-1: Rise Time/Fall Time

Symbol	Min
T _{rise}	75 ps
T _{fall}	75 ps

Intra Pair Skew (7-7)

The Intra-Pair Skew for Data Lane x Test verifies that any skew within any one differential pair in the TMDS portion of the HDMI link does not exceed the limits.

Duty Cycle Minimum / Maximum (7-8)

The clock duty cycle tests verify that the minimum /maximum duty cycle of the differential TMDS clock does not exceed the limits.

Table 4-2: Duty cycle

Symbol	Min	Мах
T _{duty-max}	40%	
T _{duty-min}		60%

Jitter (7-9)

The clock jitter test verifies that the TMDS clock does not carry more excessive jitter than $0.25T_{\text{bit}}$.

4.2.2 Voltage off tests

4.2.2.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "1.4" > "Clock" > "Voltage Off Tests".

R&S ScopeS	uite						_		-0	- 0	×
🖨 Back	Session 1	.4_20240	510_165816			Ŕ	Show Report	1	bout	•	Help
	All			Properties	Limit Manager	Results	Report Co	nfig			
	▲ HDMI 1.4	b		Channels							
	 Clock 										
	▲ All	Clock Tests				N	Skew				
		Vl + (7-2)		Sing	gleEnded Pos	Ch1 ▼	, 	ps			
		Vl - (7-2)		Sing	leEnded Neg	Ch3 🔻	·	ps			
		Rise Time (7	-4)				Retrieve Sk	ew			
		Fall Time (7	-4)	Test Setur	2						
		Intra Pair Sk	rew (7-7)	iest seta							
		Duty Cycle I	Minimum (7-8)		Vcc 3.	3	V				
		Duty Cycle I	Maximum (7-8)								
		Jitter (7-9)									
	🔺 Va	ltage Off Tes	ts								
		Voff + (7-3)									
		Voff - (7-3)									
	▼ Data										
Test C	hecked	Test Single									
Ready to run	l.										

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.

4.2.2.2 Measurements

The test measures the following:

V_{off}+ (7-3) / V_{off}- (7-3)

The V_{Off} clock-test verifies that a disabled TMDS link only allows leakage currents that are within the limits defined in HDMI CTSv1.4b.

4.3 Data tests

4.3.1 Single-ended tests

4.3.1.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "1.4" > "Data" > "Single-Ended Tests".

R&S ScopeSuite	×
Back Session 1.4_20240610_165816	R Show Report 1 About 1 Help
All	Properties Limit Manager Results Report Config
HDMI 1.4b	Channels
Clock	
Data	Skew
Single-Ended Tests	SingleEnded Pos
Vl + (7-2)	SingleEnded Neg Ch3 V 0 ps
VI - (7-2)	Retrieve Skew
Intra Pair Skew (7-7)	EDID Emulator
✓ Voltage Off Tests	
Differential Timing Tests	Wilder O GRL Allion
☐ Jitter Mask Tests	Data Lane
Inter-Pair Skew (7-6)	0 -
	Test Setup
	Vcc 3.3 V
	Export Waveforms
	Enable
	Offline Execution
Test Checked Test Single	Enable
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- 6. You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

4.3.1.2 Measurements

The test measures the following:

VI+ (7-2)

The VL Clock+ test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits. The test is passed, if the maximum VI+ < 2.9 V.

VI- (7-2)

The VL Clock-test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits.

4.3.2 Voltage off tests

4.3.2.1 Performing the tests

1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.

R&S ScopeSuite	×
Back Session 1.4_20240610_165816	R Show Report About P Help
All	Properties Limit Manager Results Report Config
HDMI 1.4b	Channels
Clock	
Data	Skew
✓ Single-Ended Tests	SingleEnded Pos
Voltage Off Tests	SingleEnded Neg Ch3 v ps
Voff + (7-3)	Retrieve Skew
Voff - (7-3)	Data Lane
Differential Timing Tests	
✓ Jitter Mask Tests	0 -
Inter-Pair Skew (7-6)	Test Setup
	Vec 2.2 V
	¥66 53 ¥
Test Checked Test Single	
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- 5. Follow the instructions of the step-by step guide.

When you have finished all steps, the compliance test runs automatically.

4.3.2.2 Measurements

The test measures the following:

V_{off}+ (7-3) / V_{off}- (7-3)

The V_{Off} clock-test verifies that a disabled TMDS link only allows leakage currents that are within the limits defined in HDMI CTSv1.4b.

4.3.3 Differential timing tests

4.3.3.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "1.4" > "Data" > "Differential Timing Tests".

R&S ScopeSuite	×
G Back Session 1.4_20240610_165816	R Show Report 1 About 1 Help
All	Properties Limit Manager Results Report Config
HDMI 1.4b	Channels
Clock	
Data	Skew
✓ Single-Ended Tests	Diff Ch1 V 0 ps
✓ Voltage Off Tests	Retrieve Skew
Differential Timing Tests	EDID Emulator
Rise Time (7-4)	
Fall Time (7-4)	Wilder 🔾 GRL 🔾 Allion
	Data Lane
Inter-Pair Skew (7-6)	0 🔻
	Test Setup
	Vcc 3.3 V
	Export Waveforms
	Enable
	Offline Execution
☑ Test Checked ► Test Single	Enable
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.

 You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

4.3.3.2 Measurements

The test measures the following:

Rise Time (7-4)/Fall Time (7-4)

The rise time T_{rise} data test verifies that the time is within the limits defined in HDMI CTSv1.4b.

The test is considered as pass, if T_{rise} > 75 ps.

Fall Time (7-4)

The fall time T_{fall} data test verifies that the time is within the limits defined in HDMI CTSv1.4b.

The test is considered as pass, if T_{fall} > 75 ps.

4.3.4 Jitter mask tests

4.3.4.1 Performing the tests

Requires option R&S RTP-K140/-K141 High-speed serial triggering 8G/16G.

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "1.4" > "Data" > "Jitter Mask Tests".

R&S ScopeSuite	×
G Back Session 1.4_20240610_165816	🖹 Show Report 🕕 About 🕐 Help
All	Properties Limit Manager Results Report Config
HDMI 1.4b	Channels
Clock	
Data	Skew
✓ Single-Ended Tests	1st Diff √ L Ch1 ▼ 0 ps
▼ Voltage Off Tests	2nd Diff √ L Ch3 ▼ 0 ps
Differential Timing Tests	Retrieve Skew
Jitter Mask Tests	EDID Emulator
Differential Voltage Mask Test (7-10)	
Jitter (7-10)	 Wilder O GRL O Allion
Inter-Pair Skew (7-6)	Data Lane
	0 *
	Test Setup
	Vcc 3.3 V
	Pixel Clock Rate 297 MHz
	Number Of Bits 250 K
Test Checked Fast Single	
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.

4.3.4.2 Measurements

The test measures the following:

Differential Voltage Mask Test (7-10)

The differential mask test confirms that the differential signal on the TMDS differential pair Dx has a region of valid data that meets or exceeds the limits of the eye opening as defined inHDMI CTSv1.4b. This means that no part of the waveform touches or crosses into the data eye.

Jitter (7-10)

The test verifies that the data jitter on the eye opening for the TMDS differential pair Dx is within the limits defined in HDMI CTSv1.4b.

4.3.5 Inter-pair skew

4.3.5.1 Performing the tests

Requires option R&S RTP-K140/-K141 High-speed serial triggering 8G/16G.

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "1.4" > "Data" > "Inter-Pair Skew (7-6)".

R&S ScopeSuite	×
G Back Session 1.4_20240610_165816	🖹 Show Report 🚺 About 👔 Help
All	Properties Limit Manager Results Report Config
HDMI 1.4b	Channels
Clock	
Data	Skew
☐ Single-Ended Tests	1st Diff ↓ Lot 1 ▼ 0 ps
✓ Voltage Off Tests	2nd Diff W the Ch3 ▼ 0 ps
Differential Timing Tests	Retrieve Skew
☐ Jitter Mask Tests	EDID Emulator
✓ Inter-Pair Skew (7-6)	
	Wilder GRL Allion
	Inter Pair
	D0D1 💌
	Test Setup
	Vcc 3.3 V
	Pixel Clock Rate 297 MHz
	Serial Pattern 001010101010101011
	Export Waveforms
	Enable
	Offline Execution
Test Checked Test Single	Enable
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- 6. You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

4.3.5.2 Measurements

The Inter-Pair Skew test for D0/D1, D1/D2, D0/D2 verifies that any skew between the differential pairs in the TMDS portion of the HDMI link does not exceed the limits defined in HDMI CTSv1.4b.

You can select the inter-pair that you want to test.

5 HDMI 2.1 TMDS tests

5.1 Test equipment

The following equipment is needed for performing HDMI 2.1 compliance tests.

Item	Description, model	Quantity
Rohde & Schwarz oscilloscope	R&S RTP with 4 channels and minimum bandwidth 8 GHz	1
Modular probes	R&S ZM160	2
Modular probe tips	R&S RT-ZMA40	2
HDMI TPA adapter	-	1
HDMI termination module	-	1
EDID emulator	-	1
DUT	HDMI 2.1 device that supports the selected type	1
Option R&S RTP-K140/-K141	Required for the following tests: Differential Voltage Mask Tests Jitter Mask Tests Inter-Pair Skew (HF 1-3)	1

Test equipment



Figure 5-1: Test setup HDMI 2.1 Clock tests

Test equipment



Figure 5-2: Test setup HDMI 2.1 Single-Ended tests



Figure 5-3: Test setup HDMI 2.1 Differential Voltage Mask Tests (HF 1-5)

Clock tests



Figure 5-4: Test setup HDMI 2.1 Inter-Pair Skew (HF 1-3)

5.2 Clock tests

5.2.1 All clock tests

5.2.1.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "HDMI 2.1 TMDS" > "Clock" > "All Clock Tests".

R&S ScopeSuite	×
Back Session 2.1 TMDS_20240610_164123	Show Report About Help
All	Properties Limit Manager Results Report Config
HDMI 2.1 TMDS	Channels
Clk	
All Clock Tests	Skew
Vl + (HF 1-1)	SingleEnded Pos Ch1 Ch1 D ps
VI - (HF 1-1)	SingleEnded Neg 🎦 Ch3 🔻 0 ps
Vswing + (HF 1-1)	Retrieve Skew
Vswing - (HF 1-1)	EDID Emulator
Rise Time (HF 1-2)	EDID Emulator
Fall Time (HF 1-2)	● Wilder ○ GRL ○ Allion
Intra Pair Skew (HF 1-4)	Test Setup
Duty Cycle Minimum (HF 1-6)	
Duty Cycle Maximum (HF 1-6)	Vcc 3.3 V
Rate (HF 1-6)	Pixel Clock Rate 594 MHz
Vswing TP1 (HF 1-7)	Export Waveforms
Jitter Worst Case Positive Skew (HF 1-7) Enable
Jitter Worst Case Negative Skew (HF 1-	7)
□ ▼ Data	Offline Execution
	Enable
Test Checked Fast Single	
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

5.2.2 Measurements

The measurements in the following chapter verify, if the measured values are within the limits defined in HDMI CTSv2.1.

The "All Clock Tests" include the following measurements:

VI + (HF 1-1)

The VL Clock+ test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits defined in the specification. The test is passed, if the maximum VI+ > 2.9 V.

VI - (HF 1-1)

The VL Clock-test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits defined in the specification. The test is passed, if the maximum VI+ is < 2.3V.

V_{swing +}

The $V_{swing +}$ test verifies that the DC voltage levels for each TMDS clock signal on the HDMI link are within the limits defined in the specification.

The test is passed, if Clock+ V_{Swing} < 600mV.

V_{swing}.

Clock rise time/ fall time

The clock rise/fall time test confirms that the rise/fall times on the TMDS signals are within the limits defined in the specification.

Table 5-1: Rise Time/Fall Time for 3.4 Gbps < R_{bit} < 6.0 Gbps at TP1

Symbol	Min
T _{rise}	75 ps
T _{fall}	75 ps

Intra Pair Skew (HF 1-4)

The Intra-Pair Skew for clock verifies that any skew within any one differential pair in the TMDS portion of the HDMI link does not exceed the limits defined in the specification.

Duty Cycle Minimum / Maximum (HF1-6)

The clock duty cycle tests confirm that the minimum /maximum duty cycle of the differential TMDS clock does not exceed the limits defined in the specification.

Table 5-2: Duty cycle for 3.4 Gbps < R_{bit} < 6.0 Gbps at TP1

Symbol	Min	Мах
T _{duty-max}	40%	
T _{duty-min}		60%

Jitter Worst Case Positive Skew (HF 1-7)

The test verifies that the TMDS Clock does not carry excessive jitter more than defined in HDMI CTSv2.1.

Jitter Worst Case Negative Skew (HF 1-7)

The test verifies that the TMDS clock does not carry excessive jitter more than defined in HDMI CTSv2.1.

5.3 Data tests

5.3.1 Single-ended tests

5.3.1.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "HDMI 2.1 TMDS" > "Data" > "Single-Ended Tests".

R&S ScopeSuite 🛛 🗸 🗆 🗸		
Back Session 2.1 TMDS_20240610_164123	🖹 Show Report 🚺 About 👔 Help	
All	Properties Limit Manager Results Report Config	
HDMI 2.1 TMDS	Channels	
Clk		
✓ All Clock Tests	Skew	
Data	SingleEnded Pos V Ch1 V 0 ps	
Single-Ended Tests	SingleEnded Neg	
Vl + (HF 1-1)	Retrieve Skew	
VI - (HF 1-1)	EDID Emulator	
Vswing + (HF 1-1)		
Vswing - (HF 1-1)	● Wilder ○ GRL ○ Allion	
Intra Pair Skew (HF 1-4)	Data Lane	
Differential Timing Tests	0 -	
Differential Voltage Mask Tests	Test Setup	
☐ ▲ Jitter Mask Tests	lest setup	
Mask Test Worst Case Positive Skew (HF 1-8)	Vcc 3.3 V	
Mask Test Worst Case Negative Skew (HF 1-8)	Pixel Clock Rate 594 MHz	
Inter-Pair Skew (HF 1-3)		
	Export Waveforms	
	Enable	
	Offline Execution	
Test Checked Test Single	Enable	
Ready to run		

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- 6. You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

5.3.2 Differential timing tests

5.3.2.1 Performing the tests

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "HDMI 2.1 TMDS" > "Data" > "Differential Timing Tests".

R&S ScopeSuite	×	
Back Session 2.1 TMDS_20240610_164123	🔥 Show Report 🚺 About 👔 Help	
All	Properties Limit Manager Results Report Config	
HDMI 2.1 TMDS	Channels	
Clk		
✓ All Clock Tests	Skew	
Data	Diff V L Ch1 V 0 ps	
▼ Single-Ended Tests	Retrieve Skew	
Differential Timing Tests	EDID Emulator	
Rise Time (HF 1-2)		
Fall Time (HF 1-2)	Wilder O GRL O Allion	
Differential Voltage Mask Tests	Data Lane	
▼ Jitter Mask Tests	0 -	
Inter-Pair Skew (HF 1-3)	Tect Setup	
	lest setup	
	Vcc 3.3 V	
	Export Waveforms	
	Enable	
	Offline Execution	
	Enable	
Test Checked Test Single		
Ready to run.		

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.

5.3.2.2 Measurements

Rise time/fall time

The data rise/fall time test verifies that the rise/fall times on the TMDS signals are within the limits defined in HDMI CTSv2.1.

5.3.3 Differential voltage mask tests

5.3.3.1 Performing the tests

Requires option R&S RTP-K140/-K141 High-speed serial triggering 8G/16G.

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- Select "HDMI 2.1 TMDS" > "Data" > "Differential Voltage Mask Tests".

R&S ScopeS	uite	□ ×
🖨 Back	Session 2.1 TMDS_20240610_164123	🖹 Show Report 🕕 About 🕐 Help
	. All	Properties Limit Manager Results Report Config
	▲ HDMI 2.1 TMDS	Channels
	▲ Clk	
	✓ All Clock Tests	Skew
	▲ Data	1st Diff w L Ch1 ▼ 0 ps
	 Single-Ended Tests 	2na Diff w Ch3 V 0 ps
	 Differential Timing Tests 	Retrieve Skew
	 Differential Voltage Mask Tests 	EDID Emulator
	Maximum Differential Voltage (HF 1-5)	
	Minimum Differential Voltage (HF 1-5)	
	 Jitter Mask Tests 	Data Lane
	Inter-Pair Skew (HF 1-3)	0 •
		Test Setup
		Vcc 3.3 V
		Pixel Clock Rate 594 MHz
		Number Of Bits 250 K
💽 Test C	hecked 🕨 Test Single	
Ready to run	L	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.

5.3.3.2 Measurements

The test measures the following:

Maximum Differential Voltage (HF 1-5)

The test verifies that the signal on each TMDS differential data pair does not exceed the limits defined in HDMI CTSv2.1.

The test is passed, if no part of the waveform exceeds the maximum differential voltage (+780mV).

Minimum Differential Voltage (HF 1-5)

The test verifies that the signal on each TMDS differential data pair does not exceed the limits defined in HDMI CTSv2.1.

The test is passed, if no part of the waveform exceeds the minimum differential voltage (-780mV).

5.3.4 Jitter mask tests

5.3.4.1 Performing the tests

Requires option R&S RTP-K140/-K141 High-speed serial triggering 8G/16G.

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "HDMI 2.1 TMDS" > "Data" > "Jitter Mask Tests".

R&S ScopeSuite — — — ×	
Back Session 2.1 TMDS_20240610_164123	R Show Report 1 About 1 Help
All	Properties Limit Manager Results Report Config
HDMI 2.1 TMDS	Channels
✓ All Clock Tests	Skew
Data	1st Diff ↓ Ch1 ▼ 0 ps
✓ Single-Ended Tests	2nd Diff Ch3 V 0 ps
Differential Timing Tests	Retrieve Skew
Differential Voltage Mask Tests	EDID Emulator
Jitter Mask Tests	
Mask Test Worst Case Positive Skew (HF 1-8)	● Wilder ─ GRL ─ Allion
Mask Test Worst Case Negative Skew (HF 1-8)	Data Lane
Inter-Pair Skew (HF 1-3)	0 *
	Test Setup
	Vcc 3.3 V
	Pixel Clock Rate 594 MHz
	Number Of Bits 250 K
Test Checked Test Single	
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".
- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.

5.3.4.2 Measurements

The test measures the following:

Mask Test Worst Case Positive Skew (HF 1-8)

The test verifies that the signal on the TMDS differential data pair Dx has a region of valid data that meets or exceeds the limits on eye opening defined in HDMI CTSv2.1.

Mask Test Worst Case Negative Skew (HF 1-8)

The test verifies that the signal on the TMDS differential data pair Dx has a region of valid data that meets or exceeds the limits on eye opening defined in HDMI CTSv2.1.

5.3.5 Inter-pair skew

5.3.5.1 Performing the tests

Requires option R&S RTP-K140/-K141 High-speed serial triggering 8G/16G.

- 1. Start the test as described in Chapter 3.5, "Starting HDMI tests", on page 17.
- 2. Select "HDMI 2.1 TMDS" > "Data" > "Inter-Pair Skew (HF 1-3)".

R&S ScopeSuite	×
G Back Session 2.1 TMDS_20240610_164123	K Show Report About Help
All	Properties Limit Manager Results Report Config
HDMI 2.1 TMDS	
	Skew
All Clock Tests	1st Diff ↓ Ch1 v 0 ps
Data	2nd Diff 🗸 Ch3 🔻 0 ps
Single-Ended Tests	Retrieve Skew
Differential Timing Tests	EDID Emulator
Differential Voltage Mask Tests	EDID Emulator
Jitter Mask Tests	● Wilder ○ GRL ○ Allion
Inter-Pair Skew (HF 1-3)	Inter Pair
	DOD1 🔻
	Test Setup
	Vcc 3.3 V
	Pixel Clock Rate 594 MHz
	Serial Pattern 001010101010101011
	Export Waveforms
	Enable
	Offline Execution
Test Checked Test Single	Enable
Ready to run.	

- 3. Enable the tests that you want to run.
- 4. Click "Test Single".

- Follow the instructions of the step-by step guide.
 When you have finished all steps, the compliance test runs automatically.
- 6. You can also run the test in offline mode, using downloaded waveforms. For details, see "Offline Execution" on page 19.