

LabVIEW driver history for the R&S® BTC Broadcast Test Center

Products:

| R&S® BTC



Driver history for LabVIEW

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1 Supported Instrument

In the following table, the supported R&S instruments and firmware versions are listed:

| Which instruments are supported? | | |
|--|---------------------------|----------------|
| Current revision of instrument driver supports these instruments and firmware versions: | | |
| Instrument | Supported Firmware | Remarks |
| BTC | 2.32 | |

2 LabVIEW History

| rsbtc Instrument Driver | | |
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| Driver history for LabVIEW | | |
| Revision | Date | Note |
| 2.32.0 | 06/2018 | <p>* New:</p> <ul style="list-style-type: none"> ATSC 3.0 standard Configure BER Treshold Clk Data En.vi Configure Fading External RF Remote Connection.vi Configure Fading External RF Coupling.vi Configure Fading External RF Parameters.vi Read Fading External RF Connection State.vi Configure DVBS2 Special PL Scrambling State.vi <p>* Modified:</p> <ul style="list-style-type: none"> All VISA resource name inputs are mandatory Exchanged Driver Core Cleaned up all the Front Panels Initialize.vi, Initialize with Options.vi, Close.vi and Utility VIs have new VI icons Configure DTV Transmission Standard.vi Configure DVBC Input Signal.vi Configure DVBS2 Special PL Scrambling.vi Configure Interferer.vi Configure BER Data Input Interface.vi |
| 1.70.1 | 12/2015 | <p>* Modified:</p> <ul style="list-style-type: none"> RSBTC_ATTR_DVBS2_CODING_MODULATION_CODING Configure DVBS2 Time Slice Coding.vi - Modulation Coding - added modulation coding settings for DVB-S2-X |
| 1.70.0 | 07/2015 | <p>* New:</p> <ul style="list-style-type: none"> Configure DVBS CID Signal.vi RSBTC_ATTR_DVBS_CID_ENABLED RSBTC_ATTR_DVBS_CID_HOST_SIGNAL RSBTC_ATTR_DVBS_CID_FREQUENCY_OFFSET Configure DVBS CID Coordinates.vi RSBTC_ATTR_DVBS_CID_CONTENT_LATITUDE_LONGITUDE_ENABLED RSBTC_ATTR_DVBS_CID_CONTENT_LATITUDE RSBTC_ATTR_DVBS_CID_CONTENT_LONGITUDE Configure DVBS CID Telephone.vi RSBTC_ATTR_DVBS_CID_CONTENT_TELEPHONE_ENABLED RSBTC_ATTR_DVBS_CID_CONTENT_TELEPHONE_NUMBER RSBTC_ATTR_DVBS_CID_CONTENT_TELEPHONE_EXTENSION_ENABLED RSBTC_ATTR_DVBS_CID_CONTENT_TELEPHONE_EXTENSION_NUMBER Enable DVBS Phase Noise.vi |

| rsbtc Instrument Driver | | |
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| | | RSBTC_ATTR_DVBS_PHASE_NOISE_ENABLED Configure DVBS Phase Noise.vi RSBTC_ATTR_DVBS_PHASE_NOISE_CW_MODE RSBTC_ATTR_DVBS_PHASE_NOISE_MAGNITUDE RSBTC_ATTR_DVBS_PHASE_NOISE_SHAPE Configure DVBS2 CID Signal.vi RSBTC_ATTR_DVBS2_CID_ENABLED RSBTC_ATTR_DVBS2_CID_HOST_SIGNAL RSBTC_ATTR_DVBS2_CID_FREQUENCY_OFFSET Configure DVBS2 CID Coordinates.vi RSBTC_ATTR_DVBS2_CID_CONTENT_LATITUDE_LONGITUDE_ENABLED RSBTC_ATTR_DVBS2_CID_CONTENT_LATITUDE RSBTC_ATTR_DVBS2_CID_CONTENT_LONGITUDE Configure DVBS2 CID Telephone.vi RSBTC_ATTR_DVBS2_CID_CONTENT_TELEPHONE_ENABLED RSBTC_ATTR_DVBS2_CID_CONTENT_TELEPHONE_NUMBER RSBTC_ATTR_DVBS2_CID_CONTENT_TELEPHONE_EXTENSION_ENABLED RSBTC_ATTR_DVBS2_CID_CONTENT_TELEPHONE_EXTENSION_NUMBER Enable DVBS2 Phase Noise.vi RSBTC_ATTR_DVBS2_PHASE_NOISE_ENABLED Configure DVBS2 Phase Noise.vi RSBTC_ATTR_DVBS2_PHASE_NOISE_CW_MODE RSBTC_ATTR_DVBS2_PHASE_NOISE_MAGNITUDE RSBTC_ATTR_DVBS2_PHASE_NOISE_SHAPE Configure DTMB Control.vi RSBTC_ATTR_DTMB_CODING_CONTROL Configure DTMB SFN.vi RSBTC_ATTR_DTMB_SFN_SIP_RESTAMPING RSBTC_ATTR_DTMB_SFN_MUTE_1ST_SIGNAL_FRAME RSBTC_ATTR_DTMB_SFN_1PPS_ROUTING RSBTC_ATTR_DTMB_SFN_DELAY_OFFSET Configure DTMB SFN Delay.vi RSBTC_ATTR_DTMB_SFN_MAX_DEVIATION_TIME RSBTC_ATTR_DTMB_SFN_STATIC_DELAY Read DTMB SFN Delay.vi RSBTC_ATTR_DTMB_SFN_NETWORK_DELAY RSBTC_ATTR_DTMB_SFN_PROCESSING_DELAY RSBTC_ATTR_DTMB_SFN_DYNAMIC_DELAY RSBTC_ATTR_DTMB_SFN_TOTAL_DELAY RSBTC_ATTR_DTMB_SFN_MAX_DEVIATION_TIME RSBTC_ATTR_DTMB_SFN_STATIC_DELAY |

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| | | RSBTC_ATTR_READ_DTMB_CODING_LOCAL_MAXIMUM_DELAY RSBTC_ATTR_DTMB_SFN_DISPATCH_TIME Enable DIRECTV Phase Noise.vi RSBTC_ATTR_DIRECTV_PHASE_NOISE Configure DIRECTV Phase Noise.vi RSBTC_ATTR_DIRECTV_PHASE_NOISE_CW_MODE RSBTC_ATTR_DIRECTV_PHASE_NOISE_MAGNITUDE RSBTC_ATTR_DIRECTV_PHASE_NOISE_SHAPE ARB Waveform Points.vi RSBTC_ATTR_ARBITRARY_WAVEFORM_GENERATION_POINTS Configure IQ Digital Out State.vi RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_ENABLED Configure IQ Digital Out IQ Swap.vi RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_IQ_SWAP Configure IQ Digital Out Level.vi RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_SET_LEVEL_VIA RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_PEAK_LEVEL RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_LEVEL Configure IQ Digital Out Sample Rate.vi RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_SAMPLE_RATE_SOURCE RSBTC_ATTR_BASEBAND_DIGITAL_IQ_OUTPUT_SAMPLE_RATE Configure Fading MIMO Relative Tap Gain Vector Set To Unity.vi RSBTC_ATTR_FADING_MIMO_RELATIVE_TAP_VECTOR_SET_TO_UNITY Configure Play File Reset.vi RSBTC_ATTR_MM_GENERATOR_PLAY_WINDOW_RESET Configure MMG Gateway Mode.vi RSBTC_ATTR_MMG_GATEWAY_MODE Get MMG Gateway Status Info.vi RSBTC_ATTR_MMG_GATEWAY_STATUS_INFO Configure MMG Gateway DVBT2 MI Input.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_USE_CASE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PROFILES RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PLP_GROUPS RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PLP RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_VV_STREAM_GENERATION Configure MMG Gateway DVBT2 MI PLP Group.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PLP_GROUP_ID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MAP_TO_PROFILE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_INPUT_TYPE Configure MMG Gateway DVBT2 MI PLP Group Input.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_INPUT_SELECT |

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| | | Configure MMG Gateway DVBT2 MI PLP Group PRBS.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_GENERATOR_DATA_RATE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_COMMON_SLOT_INTERVAL RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_NUM_CHAPTERS RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_NUM_EIT_PACKETS RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_TS_OVER_PLP_ID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_TS_OVER_CPLP RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PRBS_ID Configure MMG Gateway DVBT2 MI PLP Group PRBS Num Repetitions.vi Configure MMG Gateway DVBT2 MI PLP.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PLP_ID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PLP_TYPE Configure MMG Gateway DVBT2 MI PLP Map To Group.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MAP_TO_GROUP Configure MMG Gateway DVBT2 MI PLP Data.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_PLP_DATA RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_NEW_TS_ID Configure MMG Gateway DVBT2 MI PLP Run Length.vi Configure MMG Gateway DVBT2 MI Mode Stream PLP.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_BB_MODE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_NULL_PACKET_DELETION RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_AUTO_ISSY Configure MMG Gateway DVBT2 MI Mode Stream PLP ISSY.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_ISSY RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_MAX_BUFFER_SIZE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_BUFFER_SIZE_UNIT RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_DESIGN_DELAY Configure MMG Gateway DVBT2 MI Mode Stream PLP In Band Signalling.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_INBAND_SIGNALLING RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_INBAND_SIGNALLING_A RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_MODE_STREAM_INBAND_SIGNALLING_B Configure MMG Gateway DVBT2 MI BICM Parameters.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_FEC_FRAME RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_CODE_RATE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_CONSTELLATION RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_CONSTELLATION_ROTATION Configure MMG Gateway DVBT2 MI BICM Time Interleaver Parameters.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_TIME_INTERLEAVER_TYPE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_FRAME_INTERVAL RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_TIME_INTERLEAVER_LENGTH RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_FIRST_FRAME_INDEX |

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| | | Configure MMG Gateway DVBT2 MI BICM Max Blocks.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_AUTO_NUM_BLOCKS_MAX RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_BICM_NUM_BLOCKS_MAX Configure MMG Gateway DVBT2 MI Framing OFDM Parameters.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_CHANNEL_BANDWIDTH RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_FFT_SIZE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_GUARD_INTERVAL RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_PILOT_PATTERN Configure MMG Gateway DVBT2 MI Framing OFDM.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_T2_FRAMES_PER_SUPER_FRAME RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_DATA_SYMBOLS_PER_T2_FRAME RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_FRAMING_SUBSLICES_PER_T2_FRAME Configure MMG Gateway DVBT2 MI Transmission System.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_SCHEDULING RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_TRANSMISSION_SYSTEM RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_PEAK_TO_AVERAGE_POWER_RATIO RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_FUTURE_EXTENSION_FRAMES RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_TIME_FREQUENCY_SLICING Configure MMG Gateway DVBT2 MI Transmission System Identification.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_NETWORK_ID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_CELL_ID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_ID Configure MMG Gateway DVBT2 MI Transmission Future Extension Frames.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_FEF_LENGTH RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_FEF_TYPE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_FEF_INTERVAL Configure MMG Gateway DVBT2 MI Transmission L1 Parameters.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_T2_VERSION RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_POST_MODULATION RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_REPETITION RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_POST_EXTENSION RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_POST_SCRAMBLER RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_BIAS_BALANCING_BITS RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_T2_BASE_LITE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_T2_SYSTEM_L1_RF_FREQUENCY Configure MMG Gateway DVBT2 MI Output Parameters.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_T2_TIMESTAMP_MODE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_TIME_OFFSET Configure MMG Gateway DVBT2 MI Output Profile.vi RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_T2_MI_PID |

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| | | RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_T2_MI_SID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_T2_MI_TS_TYPE RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_T2_MI_PMT_PID RSBTC_ATTR_MMG_GATEWAY_DVBT2MI_OUTPUT_T2_MI_DATA_RATE Configure MMG Gateway DVBT2 MI Output Restart.vi Configure Signal Polarity.vi Configure DVBS2 Special PL Scrambling.vi Configure DVBS2 S2-X.vi * Modified Configure ISDBT Special Settings.vi - add "Unused" option in "AC data" controls |
| 1.62.0 | 02/2015 | * For firmware version 1.62 * New Configure Distortion Limiter.vi Configure Level With Units.vi Configure ISDBT EEW System.vi Configure ISDBT EEW Parameters.vi Configure Fading MIMO Correlation Matrix Table.vi Configure Fading MIMO Relative Tap Gain Vector Table.vi Load Analog TV BTSC Library.vi Analog TV BTSC Library Catalog.vi Analog TV BTSC Library Catalog Length.vi RSBTC_ATTR_IP_INPUT RSBTC_ATTR_IP_INPUT_ALIAS RSBTC_ATTR_IP_INPUT_TYPE RSBTC_ATTR_IP_INPUT_MULTICAST_ADDRESS RSBTC_ATTR_IP_INPUT_PORT RSBTC_ATTR_IP_INPUT_IGMPV3_SOURCE_ADDRESS RSBTC_ATTR_IP_INPUT_PING_SOURCE_ADDRESS RSBTC_ATTR_DVBT_SFN_1PPS_ROUTING RSBTC_ATTR_DVBT_SFN_OFFSET RSBTC_ATTR_DVBC2_MODE_STREAM_ADAPTATION_PLP_BUNDLING RSBTC_ATTR_ISDBT_SFN_1PPS_ROUTING RSBTC_ATTR_ISDBT_SFN_OFFSET RSBTC_ATTR_ISDBT_EEW_STATE RSBTC_ATTR_ISDBT_EEW_SIGNAL_TYPE RSBTC_ATTR_ISDBT_EEW_AREA_INFORMATION RSBTC_ATTR_ISDBT_EEW_APPLY RSBTC_ATTR_ISDBT_EEW_NUMBER_OF_EPICENTERS RSBTC_ATTR_ISDBT_EEW_EPICENTER_INFORMATION_TYPE RSBTC_ATTR_ISDBT_EEW_EPICENTER_WARNING_ID RSBTC_ATTR_ISDBT_EEW_EPICENTER_LATITUDE |

| rsbtc Instrument Driver | | |
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| Driver history for LabVIEW | | |
| Revision | Date | Note |
| | | RSBTC_ATTR_ISDBT_EEW_EPICENTER_LONGITUDE RSBTC_ATTR_ISDBT_EEW_EPICENTER_DEPTH RSBTC_ATTR_ISDBT_EEW_EPICENTER_OCCURRENCE_TIME RSBTC_ATTR_ISDBT_EEW_EPICENTER_APPLY RSBTC_ATTR_DVBS2_CODING_S2_X RSBTC_ATTR_DVBS2_CODING_S2_X_SUPER_FRAME RSBTC_ATTR_DVBS2_CODING_S2_X_CHANNEL_BONDING RSBTC_ATTR_READ_CMMB_MEASURED_REQUIRED_DATA_RATE RSBTC_ATTR_READ_CMMB_STREAM_FORMAT RSBTC_ATTR_TDMB_SFN_NETWORK_COMPENSATION RSBTC_ATTR_TDMB_SFN_TX_COMPENSATION_DELAY RSBTC_ATTR_TDMB_SFN_TX_PROCESSING_DELAY RSBTC_ATTR_TDMB_SFN_TX_DELAY RSBTC_ATTR_TDMB_SFN_STATIC_DELAY RSBTC_ATTR_TDMB_SFN_TOTAL_DELAY RSBTC_ATTR_TDMB_SFN_MAX_DEVIATION_TIME RSBTC_ATTR_TDMB_SFN_1PPS_ROUTING RSBTC_ATTR_ANALOG_TV_BTSC_LOAD_FILE RSBTC_ATTR_RX_DISTORTION_NONLINEARITY RSBTC_ATTR_TX_DISTORTION RSBTC_ATTR_RX_DISTORTION RSBTC_ATTR_RX_DISTORTION_NONLINEARITY_PHASE RSBTC_ATTR_RX_DISTORTION_NONLINEARITY_GAIN RSBTC_ATTR_FADING_STANDARD_DELAY_FREQUENCY_SHIFT RSBTC_ATTR_FADING_STANDARD_DELAY_FREQUENCY_SPREAD RSBTC_ATTR_FADING_MIMO_MATRIX_VALUE_COLUMN_MAGNITUDE RSBTC_ATTR_FADING_MIMO_MATRIX_VALUE_COLUMN_PHASE RSBTC_ATTR_FADING_MIMO_MATRIX_VALUE_CONFLICT_COLUMN RSBTC_ATTR_FADING_MIMO_MATRIX_ACCEPT RSBTC_ATTR_FADING_MIMO_RELATIVE_TAP_VECTOR_GAIN RSBTC_ATTR_FADING_MIMO_RELATIVE_TAP_VECTOR_PHASE RSBTC_ATTR_RF_SIGNAL_LEVEL_DIGITAL_ATTENUATION RSBTC_ATTR_MM_GENERATOR_IP_INPUT RSBTC_ATTR_MM_GENERATOR_IP_INPUT_ALIAS RSBTC_ATTR_MM_GENERATOR_IP_INPUT_TYPE RSBTC_ATTR_MM_GENERATOR_IP_INPUT_MULTICAST_ADDRESS RSBTC_ATTR_MM_GENERATOR_IP_INPUT_PORT RSBTC_ATTR_MM_GENERATOR_IP_INPUT_IGMPV3_SOURCE_ADDRESS RSBTC_ATTR_MM_GENERATOR_IP_INPUT_PING_SOURCE_ADDRESS RSBTC_ATTR_SYSTEM_REBOOT RSBTC_ATTR_SYSTEM_RESTART |

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| | | RSBTC_ATTR_SYSTEM_SHUTDOWN RSBTC_ATTR_BER_IP_INPUT RSBTC_ATTR_BER_IP_INPUT_ALIAS RSBTC_ATTR_BER_IP_INPUT_TYPE RSBTC_ATTR_BER_IP_INPUT_MULTICAST_ADDRESS RSBTC_ATTR_BER_IP_INPUT_PORT RSBTC_ATTR_BER_IP_INPUT_IGMPV3_SOURCE_ADDRESS RSBTC_ATTR_BER_IP_INPUT_PING_SOURCE_ADDRESS RSBTC_ATTR_MM_GENERATOR_TRANSPORT_STREAM_REMUX RSBTC_ATTR_MM_GENERATOR_TRANSPORT_STREAM_PLAYER_TYPE RSBTC_ATTR_MM_GENERATOR_TRANSPORT_STREAM_PLAYER_INPUT_STATE RSBTC_ATTR_MM_GENERATOR_OUTPUT_CONFIG_DATA_RATE RSBTC_ATTR_BER_GATING_TIME_USER * Modified Configure Signal Type.vi Read DVBT2 SFN Timestamp Parameters.vi Read CMMB Input Signal Data Rate.vi Configure Analog TV Sound Modulation.vi Configure IQ Digital Input Level.vi Configure Interferer.vi Distortion File Operations.vi Configure Fading Standard Set.vi Configure Fading Path Profile Table Numeric.vi Configure IQ Digital Output.vi RSBTC_ATTR_ARBITRARY_WAVEFORM_GENERATION_SAMPLES - changed data type to DBL RSBTC_ATTR_RF_SIGNAL_IQ_DIGITAL_OUTPUT_CONNECTED_DEVICE - changed command RSBTC_ATTR_RF_SIGNAL_LEVEL_UNIT RSBTC_ATTR_AUDIO_BC_FM_AUDIO_PLAYER_ATTENUATION_1 - fixed range RSBTC_ATTR_AUDIO_BC_FM_AUDIO_PLAYER_ATTENUATION_2 RSBTC_ATTR_AUDIO_BC_AM_AUDIO_PLAYER_ATTENUATION RSBTC_ATTR_AUDIO_BC_AM_AUDIO_PLAYER_ATTENUATION RSBTC_ATTR_RF_SIGNAL_MODULATION_WIDEBAND_IQ_GAIN - changed command RSBTC_ATTR_FADING_GENERAL_SIGNAL_DEDICATED_TO * Deleted Configure Distortion Nonlinearity Phase Table.vi Configure Distortion Frequency Response Phase Table.vi Configure Level Step.vi Configure Frequency Step.vi RSBTC_ATTR_DVBT_SPECIAL_MODULATION_DATA_CARRIER RSBTC_ATTR_DVBT_SPECIAL_MODULATION_PILOT_CARRIER RSBTC_ATTR_DVBT_SPECIAL_MODULATION_TPS_CARRIER |

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| | | RSBTC_ATTR_DVBT_SPECIAL_DATA_CARRIER RSBTC_ATTR_DVBT_SPECIAL_PILOT_CARRIER RSBTC_ATTR_DVBT_SPECIAL_TPS_CARRIER RSBTC_ATTR_DVBT_SPECIAL_ALL_CARRIERS_EQUAL_CW RSBTC_ATTR_READ_CMMB_MAX_USEFUL_DATA_RATE RSBTC_ATTR_TX_ADJUST_IQ_MODULATOR RSBTC_ATTR_DTMB_SFN_DELAY_ADJUST RSBTC_ATTR_DTMB_SFN_DISTRIBUTION_DELAY RSBTC_ATTR_READ_DTMB_CODING_LOCAL_DELAY RSBTC_ATTR_DTMB_SFN_RF_DELAY RSBTC_ATTR_DTMB_SFN_FREQUENCY_OFFSET RSBTC_ATTR_DTMB_SFN_TX_POWER RSBTC_ATTR_DTMB_SFN_TX_ADDRESS RSBTC_ATTR_DVBS2_CODING_CODE_RATE RSBTC_ATTR_DVBS2_CODING_CONSTELLATION RSBTC_ATTR_AUDIO_BC_DIGITAL_IQ_INPUT_STATE RSBTC_ATTR_RF_SIGNAL_LEVEL_STEP_INCREMENT RSBTC_ATTR_RF_SIGNAL_LEVEL_STEP_STATE RSBTC_ATTR_FADING_PARAMETER_SET RSBTC_ATTR_FADING_USER_INSERT_LOSS |
| 1.50.0 | 02/2014 | * For firmware version 1.50 - Initial version |

About Rohde & Schwarz

Rohde & Schwarz is an independent group of companies specializing in electronics. It is a leading supplier of solutions in the fields of test and measurement, broadcasting, radiomonitoring and radiolocation, as well as secure communications. Established more than 75 years ago, Rohde & Schwarz has a global presence and a dedicated service network in over 70 countries. Company headquarters are in Munich, Germany.

Environmental commitment

- Energy-efficient products
- Continuous improvement in environmental sustainability
- ISO 14001-certified environmental management system



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