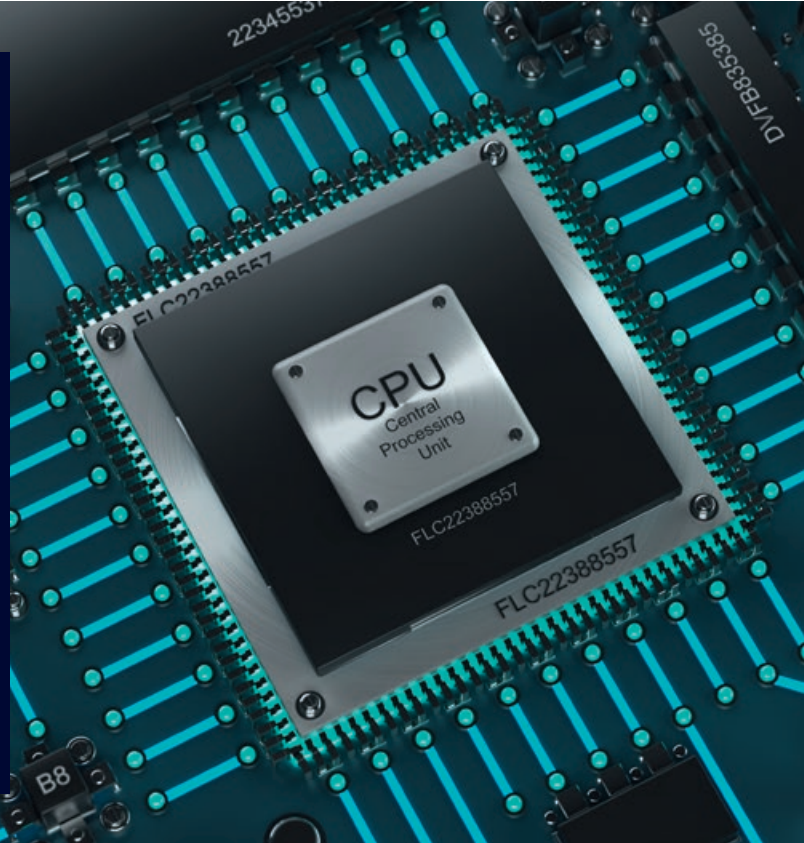


ELECTRONIC DESIGN AND TEST DAY

FEBRUARY 20, 2020

ROHDE & SCHWARZ

Make ideas real



AGENDA

Time	Track 1 DIGITAL DESIGN VERIFICATION AND DEBUG	Track 2 POWER CONVERTER DESIGN AND TEST
09:00 – 09:15	Welcome Patricio Duenas, Martin Stumpf, Rohde&Schwarz	
09:15 – 10:00	SI-PI-EMI: Three problems for every electronic design, Prof. Christian Schuster, TUHH	
10:00 – 10:30	Coffee break	
10:30 – 11:15	Challenges and best practices in the design of embedded systems Hermann Ruckerbauer, EKH	Dynamic limitations in power supplies Bernd Geck, Texas Instruments
11:15 – 12:45	Lunch break and exhibition	
12:45 – 13:30	Gain more insights in your system with precise clock jitter analysis and jitter decomposition Guido Schulze, Martin Stumpf, Rohde&Schwarz	Practical aspects designing compact high-efficiency power supplies – A real-world-example Felix Heinrich, Rohde&Schwarz
13:30 – 14:15	Realtime signal integrity debugging of highspeed interfaces up to 10 Gbps Guido Schulze, Alexander Küllmer, Rohde&Schwarz	Basics and practical aspects for high-voltage probing Tim Paasch-Colberg, Mathias Hellwig, Rohde&Schwarz
14:15 – 14:45	Coffee break	
14.45 – 15:30	Measurement and debug of high-speed interconnects using TDR Alexander Küllmer, Volker Herrmann, Rohde&Schwarz	Tips and tricks on how to verify control loop stability Andreas Ibl, Marcus Sonst, Rohde&Schwarz
15:30 – 15:45	Coffee break	
15:45 – 16:30	Measurement tools to verify and debug power integrity and PDN impedance Alexander Küllmer, Volker Herrmann, Rohde&Schwarz	
16.30 – 17:15	EMC Analysis – In-system-verification methods Alexander Küllmer, Rohde&Schwarz	
17:15 – 17:30	Wrap-up and farewell	