## **Application Note**

# SMALL CELL TESTING IN FR2

Comprehensive test solutions guide for production and R&D

## **Products:**

- ► R&S<sup>®</sup>CMP200
- ► R&S<sup>®</sup>CMQ200
- ► R&S<sup>®</sup>CMPHEAD30/CMPHEAD50
- ► R&S®WMT
- ► R&S®ATS1000
- ► R&S®AMS32-BST

Yong Shi | 1SL395 | Version 1e | 06.2023 http://www.rohde-schwarz.com/appnote/1SL395

- ► R&S®FSW
- ► R&S<sup>®</sup>SMW200A
- ► R&S®RTP/RTO
- ► R&S®RT-ZPR40/RT-ZPR20
- ► R&S<sup>®</sup>ZNA/ZNBT

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## Abstract

Small cell is a compact base station with smaller form factor and lower transmission power in comparison to the conventional macro base station. It covers relatively small area and serves less users. Usually, small cell can be integrated into the existing mobile network. By the evolution of radio access technology, the role of small cell has been changing through the evolution path. In the 2G/3G time, its role was to provide coverage in corner cases. Later during LTE, networks are not just providing coverage but capacity too. Small cells were then used to provide the addition capacity where required without adding additional spectrum. Now in 5G era, network operators use densification as an important strategy to provide seamless 5G services which demand coverage, capacity and performance too. With use cases requiring the 5G millimeter wave (mmW) rollouts, it makes sense to use small cells for densification due to the propagation characteristics of mmW.

In this application note, we will shed light on the testing aspects of a small cell throughout the product life cycle with particular focus on the production test solution for the small cell device under test (DUT) in FR2 (frequency range 2, mmW frequency band) in Over the Air (OTA) environment for option 6 split based on radio communication tester R&S®CMP200 and OTA chamber R&S®CMQ200. The document is complemented with more insights into test solutions used in typical R&D test applications towards the second half of the application note.

The application note is structured in the following way:

Chapter 1 briefly introduces small cells and OTA background

Chapter 2 describes in great details about the production test solution for the small cell testing in FR2 in OTA environment with R&S<sup>®</sup>CMP200, the Radio Communication Tester and OTA chamber R&S<sup>®</sup>CMQ200 with the automation framework R&S<sup>®</sup>WMT controlling the instrument, chamber and the small cell (DUT)

Chapter 3 presents other R&D testing aspects for a small cell

The following abbreviations are used for R&S® products throughout the whole application note:

- R&S<sup>®</sup>CMP200 Radio Communication Tester is referred to as CMP200
- R&S<sup>®</sup>CMQ200 Shielding Cube mmW is referred to as CMQ200
- R&S<sup>®</sup>CMPHEAD30 or R&S<sup>®</sup>CMPHEAD50 Remote Radio Head is referred to as CMPHEAD30 or CMPHEAD50, or RRH in short
- R&S<sup>®</sup>WMT Wireless Manufacturing Test Tool is referred to as WMT
- R&S<sup>®</sup>CMsquares Web-based user interface of CMP200 is referred to as CMsquares
- R&S<sup>®</sup>WinIQSIM2 signal generation software is referred to as WinIQSIM2
- R&S<sup>®</sup>VSE Vector Signal Explorer is referred to as VSE

## 1 Background

## 1.1 Small Cell

According to 3GPP TS38.104 [1], base stations (BS) are categorized into three types based on the BS to UE minimum distance and BS to UE minimum coupling loss (MCL), namely, Macro, Micro and Pico cells (see Table 1-1). Micro and Pico cells fall under small cell category.

Base Station Type	Coverage	BS to UE minimum distance (m)	BS to UE coupling loss (dB)
Macro Cell	Wide Area	35	70
Micro Cell	Medium Range	5	53
Pico Cell	Local Area	2	45

Table 1-1 Base station category (3GPP TS38.104 [1])

Looking at the small cell deployment, all-in-one integrated small cell provides layer 1 to 3 and RF functionality within a single unit and interface with the core network in the past. By entering 5G era, the latest technical trend shows that small cell is going to be deployed in the form of so-called virtualized disaggregated networks. It simply means that the functions of a small cell are split into different options as shown in Figure 1-1 by having standardized open interfaces. This approach enables more scaling efficiency of the network and to make it easier for the service providers to align with their customer needs and available infrastructure. The different splits allow the versatile use cases to be deployed in different scenarios, indoor or outdoor, density urban or suburb area, public or private to meet the end customer requirements in terms of latency, capacity, QoS and security aspects.

Industry consortiums like 3GPP, Small Cell Forum (SCF), O-RAN Alliance and CPRI/OBSAI standardize the disaggregation architectures, such as option 2, option 6, option 7.2x and option 8 respectively.



Figure 1-1 Split options for disaggregated Radio Access Network (RAN)

Option 2 is a split of centralized unit (CU) and distributed unit (DU) which is literally the separation of user and control plane.

Option 6 has a split between PHY and MAC layer which is promoted by SCF. Within option 6 split, the network FAPI (nFAPI) interface allows service providers to mix the S-DUs and S-CUs from different vendors

to connect to any small cell radio unit (S-RU) (shown in Figure 1-2).



Figure 1-2 SCF split 6

In addition, O-RAN defines the low PHY and high PHY split option 7.2x within the PHY to support various fronthaul requirements and hence yield higher virtualization benefits.



#### Figure 1-3 O-RAN split 7.2x

In this application note, test solutions for option 6 split are the focus.

## 1.2 Over the Air Testing

### 1.2.1 Motivation

In contrast to the conventional conducted test mode (DUT is connected physically to the measurement instrument via RF cable), OTA is the only methodology for a small cell product operating in FR2. This has good reasons. Massive MIMO and beamforming are the key features for the FR2 DUT. With short wavelength in mmW frequency band, the antenna elements used to steer the beam are placed more densely. This is nearly impossible to put connectors onto it for the conducted testing. Even if it were possible, due to the massive nature of the antenna elements, to connect all the connectors one after the other becomes impractical. From economy aspect, the cost is chased higher if everything is done in conducted mode with respect to the measurement equipment and all connected accessories (e.g. RF cables etc.).

## 1.2.2 OTA System Calibration

The aim of OTA system calibration is to determine the insertion loss occurred on the connected cables, the components in both Transmitter (Tx) path and Receiver (Rx) path, antenna loss, and free space path loss (FSPL) in the air throughout the entire signal propagation path. In other word, one end of the calibration plane is the signal source where signal is transmitted (e.g. at the Tx port of the signal generator) and the other end of the calibration plane is the signal sink where the signal is received (e.g. at the Rx port of the spectrum analyzer).

The calibration is frequency dependent and has to be performed prior to tests. There are several ways to conduct the calibration which differ in the used measurement equipment, using the signal generator and spectrum analyzer (suitable for modulated signals), vector network analyzer (suitable for CW signal) or simply using golden reference device. A conceptual illustration of OTA system calibration using the signal generator and spectrum analyzer is given in Figure 1-4. Details of the OTA system calibration can be further referred in Chapter 3 of the white paper (Demystifying over-the-air (OTA) testing) [2].





As the outcome of the OTA system calibration, frequency dependent compensation (FDC) is obtained that consists of the information about the measured frequency and its corresponding signal loss in dB over the whole chain. The more frequency points that have been calibrated, the more precise that the compensated pathloss values are. The pathloss between the adjacent frequencies is linearly interpolated.

## **2 Test Solution for Production**

In this chapter, a one-box solution based on CMP200, CMQ200, RRH for small cell production testing operating in FR2 frequency band controlled by R&S WMT automation framework is described.

In order to allow S-RU to be operated in isolated mode without the accompanying S-DU (see the split in Figure 1-2) in disaggregated network, the S-RU should essentially support test mode, i.e. Thin MAC [3], to generate and analyze 3GPP defined test model signals. This approach exposes cost and test efficiency advantages in comparison to the End2End solution that involves the DU emulator. Therefore, in this document, the support of test mode on S-RU is assumed.

## 2.1 Used Hardware and Software

The complete small cell test solution for production is shown in Figure 2-1.



Figure 2-1 Overview of FR2 small cell OTA solution for production

### 2.1.1 CMP200 and Remote Radio Head (RRH) CMPHEAD30/CMPHEAD50

CMP200 is a one-box radio communication tester suitable for 5G FR2 testing in non-signaling mode. It contains general purpose analyzer covering frequency range from 4 to 20 GHz and ARB generator. Replaying of predefined waveforms (-100 dBm) with frequency range from 6 to 20 GHz is supported. With three switchable ports with 1 GHz bandwidth each, the measurement speed is accelerated accordingly.

For testing 5G FR2 mmW band, a remote radio head (RRH) CMPHEAD30 facilitating the frequency range extension to FR2 mmW band (24 GHz to 44 GHz) is required. Thanks to the built-in IF frontend, up to three RRHs can be connected to a CMP200. Each RRH has two 2.4 mm RF interfaces that can be activated one after the other via solid-state switches. The overall setup with three RRHs provides up to  $3 \times 2 = 6$  paths (TDD mode), giving users the flexibility to operate multi-DUT and multi-array test concepts.



#### Figure 2-2 CMP200 and CMPHEAD30 RRH

This hardware tandem mentioned above was also validated for FR2 small cell testing by Qualcomm<sup>®</sup>. Please find the details in the <u>press release</u>.

Alternative to CMPHEAD30, CMPHEAD50 covering FR2 frequency range from 22 GHz to 50 GHz can be adopted for FR2 small cell testing as well. The additional Rx low-noise input ensures wider dynamic range, thus enhances the Rx sensitivity. Thanks to the individual external local oscillator (LO) inputs for both Tx and Rx paths, superb EVM performance for RF measurements can be achieved. In Figure 2-3, top, front and rear view of a CMPHEAD50 RRH are presented.



Figure 2-3 CMPHEAD50 RRH (top, front and rear view)

In the remaining of this document, the captured screenshots are based on CMPHEAD30.

## 2.1.2 CMQ200

CMQ200 is the OTA shielding chamber from R&S. It supports the mmW range from 20 GHz to 77 GHz. In combination with CMP200 and RRH, it is ideal for RF parametric tests on 5G FR2 devices in non-signaling mode.



Figure 2-4 CMQ200 shielding cube

For FR2 measurement, dual polarized Vivaldi antenna (probe antenna) is utilized in CMQ200.

### 2.1.3 Source of Frequency Reference and Timing Synchronization

An extra instrument or device needs to be adopted to provide the following functionality to the whole small cell test solution:

- ► Frequency reference (10 MHz reference)
- 1 pulse per second (PPS) signal that servers accurate timing signal for precise synchronization of each small cell

As an example, R&S signal generator R&S<sup>®</sup>SMBV100A (Figure 2-5) can well serve the functionalities mentioned above.



Figure 2-5 R&S vector signal generator R&S<sup>®</sup>SMBV100A

## 2.1.4 Wireless Manufacturing Test (WMT)

WMT is Python based software solution for chipset and module RF testing that is installed on external control PC. It is tailored for high volume production testing and R&D applications. Modular architecture enables the flexible integration and minimum time-to-market.



Figure 2-6 WMT architecture

As illustrated in Figure 2-6, WMT provides a single point for instrument control (e.g. CMP200), DUT control, testcase creation (Test Plan Editor), testcase execution (Test Runner) and its associated limit check and report collection. If needed, WMT integrates also the chamber control as well.

Two GUIs are included in WMT, namely, WMT Test Plan Editor and WMT Test Runner.

In the Test Plan Editor (Figure 2-7), as the name already implies, a test plan can be newly created or the existing one can be modified. The expected limits of the test items can be defined. In each test plan, it is possible to include multiple test cases with individual parameter settings.

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NR_FR2_BS TM3.1	NR_FR2_BS	TX		Bandwidth	100 ~	MHz
				Beam Id RX	11	]
				Beam Id TX	11	]
				Capture Time TX	20.1	ms
				Channel	2054999	]
				Measurements TX	power, modulation, aclr, r	]
				Phase Compensation	True	]
				Power Mode	Target ~	
				Power TX	10.0	dBm
				Rscmsa Aclr File	None	]
				Rscmsa M5g File	None	]
				Rscmsa Sem File	None	]
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#### Figure 2-7 WMT Test Plan Editor

WMT Test Runner (Figure 2-8) provides the environment for the test execution of a selected test plan as well as multi-DUT parallel testing. After each test plan is executed, test report can be inspected. Debug log associated to the test run can be checked in the test runner, if required.



Figure 2-8 WMT Test Runner

#### 2.1.5 CMsquares

CMsquares is the Web-based user interface of CMP200. It comes along with the CMP200 firmware installation. The CMsquares is accessible from the browser of any remotely connected PC that is located in the same network as CMP200 (i.e. IPs belong to the same network segment) by entering the CMP200 IP address in the browser address field or locally on CMP200.

For small cell testing, the captured IQ samples are presented in the IQ Recorder square as shown in Figure 2-9.



Figure 2-9 IQ Recorder square in CMsquares (CMP200 Web GUI)

## 2.1.6 R&S CM Spectrum Analyzer (RSCMSA)

RSCMSA is software (license option KN601 is required) for FR2 downlink modulation and spectrum analysis on CMP200.

The software distribution is in a form of windows installer which can be downloaded from Gloris customer portal of R&S (<u>https://gloris.rohde-schwarz.com/</u>).

The installation is straightforward and the steps can be found in Appendix A of this document.

Figure 2-10 illustrates the measurement concept between CMP200 (with RSCMSA software) and WMT. The WMT configures and controls the instrument via SCPI commands. On CMP200, the captured IQ data is then parsed to RSCMSA software for spectrum analysis. The analysis results are then fetched by WMT and presented in WMT in the end. The biggest advantage of this approach is that the parallel processing of multiple jobs is possible. That means, while RSCMSA analyzing the IQ data, the next IQ capturing can be already started independently. This enhances the efficiency of the testing.



Figure 2-10 Measurement concept of CMP200/WMT with RSCMSA software

## 2.1.7 VSE / WinIQSIM2

VSE is a software tool for signal analysis installed on control PC.

IQ samples recorded on CMP200 can be optionally transferred to VSE for post processing. The signal analysis results are then presented in VSE as shown in Figure 2-11.

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Figure 2-11 Signal analysis in VSE

As indicated in Figure 2-12, VSE for signal analysis is per default enabled in section 'vsa' of 'testconfig.ini' config file in WMT Test Runner. This provides finer granularity of signal analysis for debugging purpose especially for R&D applications.



#### Figure 2-12 Enable the VSE visualization in 'testconfig.ini' in WMT

In production application, it is however recommended to disable the signal analysis in VSE in order to speed up the production process.

WinIQSIM2 is the tool to generate the ARB wave file which is necessary for the small cell receiver tests that is installed on the control PC as well. The generated waveform file is then transferred to and later on played back on CMP200. The configuration and generation of the waveform file is controlled by WMT. It does not require direct user interaction on WinIQSIM2. WinIQSIM2 application only needs to be manually launched and run in the background before it is called by WMT.

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	Export Settings to Analyzer
Link Direction Uplink	Test Models
Node	Users/BWPs
Scheduling	Output/Power
Time Plan	
5G NR	



### 2.1.8 DUT Control Software

DUT control software evtl. the driver for DUT needs to be installed on the control PC. This is however DUT manufacture proprietary. For example. QMSL library from Qualcomm. The user has to ensure that the environment with respect to the DUT control is setup properly. Based on this prerequisite, the corresponding WMT version that contains this specific DUT control should be applied.

Please contact your local R&S support team to get more detailed information on the suitable WMT distribution that matches your DUT.

## 2.2 Getting Started

#### 2.2.1 Cabling

Figure 2-14 gives an overview of the system cabling of the test solution described in Chapter 2.1.



Figure 2-14 Overview of system cabling

Details of each labeled connection can be referred as follows:

1. Cabling of 10 MHz reference signal

Since the used signal source might differ to R&S<sup>®</sup>SMBV100A, the name of source port shown in Figure 2-15 is exemplary.



Figure 2-15 Cabling of 10 MHz reference between R&S®SMBV100A and CMP200

2. Cabling of 1 PPS clock signal

Since the used signal source might differ to R&S<sup>®</sup>SMBV100A, the name of source port shown in Figure 2-16 is exemplary.



Figure 2-16 Cabling of 1 PPS clock between R&S<sup>®</sup>SMBV100A and small cell DUT

3. Cabling between CMP200 and CMPHEAD30 (RRH)



Figure 2-17 Cabling between CMP200 and CMPHEAD30 (RRH)

4. Cabling between CMPHEAD30 (RRH) and Vivaldi

The cross polarized Vivaldi probe antenna is placed in the CMQ200 chamber and accessible from outside of the chamber through an RF feedthrough which has two separate RF connectors connecting to horizontal polarization and the vertical polarization of the Vivaldi antenna, respectively.



Figure 2-18 Cabling between CMPHEAD30 (RRH) and Vivaldi antenna through an RF feedthrough

5. Cabling of trigger signal between the small cell DUT and CMP200

This connection is required if receiver tests are performed.



Figure 2-19 Cabling of trigger signal between small cell DUT and CMP200

6. Connection between control PC and CMP200

Control PC (WMT) configures and controls the CMP200 via IP interface. Therefore, LAN connection between both of them are required.



Figure 2-20 Cabling between CMP200 and control PC

7. Connection between control PC and DUT

Used interface for DUT control is proprietary (via USB, COM port, IP etc.). It is the integrated function of WMT delivery for each individual implementation.

### 2.2.2 System IP Configurations

Ensure that the IP of windows control PC and CMP200 are in the same network segment. Either both of them are assigned automatically by the DHCP server from the connected network or set manually with the static IPs.

In this section, the IP settings in CMsquares on CMP200 is highlighted in Figure 2-21. For more details about the CMP200 operations, please refer to CMP200 user manual [4].



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MAC Address:	00:E0:33:07:C9:51		
Status:	Connected		
<ul> <li>IPv4 Settings</li> </ul>			
IPv4 Method	Manual	- 5	
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	192.168.1.200	255.255.255.0	
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<ul> <li>IPv6 Settings</li> </ul>			
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	Address	Netmask	Gateway
	fe80::4bc2:91a	64	
	DNS Servers		
	🖋 Edit		

Figure 2-21 Modification of the IP settings in CMsquares on CMP200

### 2.2.3 Run Test and Collect Result

In this section, a step-by-step guide is given to show the workflow of testing using WMT. Moreover, the available measurement results are also introduced here.

- 1. Make sure that the DUT is powered on and is in the right idle operation mode (e.g. DUT is detected by its DUT control software)<sup>1</sup>
- On control PC, launch WMT Test Plan Editor GUI. Create the test plan and make proper settings for each test case from there (Figure 2-22). The test case relevant parameter settings can be found in Table 2-1.

🚸 RS-WMT Test Plan Editor					-		
File Edit							
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				Power Mode	Target ~	]	
				Power TX	10.0	dBm	1
				Rscmsa Aclr File	None	]	
				Rscmsa M5g File	None	]	
				Rscmsa Sem File	None	]	
				Subcarrier Spacing	120 ~	kHz	
IR_FR2_BS ~ NR_FR2_BS	Ade	d Del Copy	Paste Up Down	Test Model	TM1.1 ~	1	

<sup>&</sup>lt;sup>1</sup> This is proprietary which means that the status differs from vendor to vendor

#### Figure 2-22 Test plan and settings in WMT Test Plan Editor

Designation	Description	Values
Additional Tasks	Adjust the expected nominal power level automatically on CMP200 (Auto Ranging)	Enable / Disable
Band	NR operating band	
Bandwidth	Channel bandwidth	
Beam Id RX	Set the receiver beam ID on DUT to ensure the boresight of the antenna is perpendicular to the antenna plane of the DUT and centered to the Vivaldi test antenna in CMQ200	The value selection is product specific that can be recommended by the antenna designer of the product.
Beam Id TX	Set the transmitter beam ID on DUT to ensure the boresight of the antenna is perpendicular to the antenna plane of the DUT and centered to the Vivaldi test antenna in CMQ200	The value selection is product specific that can be recommended by the antenna designer of the product.
Capture Time Tx	Time duration of IQ recording	
Channel	NR ARFCN	
Measurement Tx	Selection of Tx test	Power, modulation, ACLR, Spurious Emission Mask (SEM)
Phase Compensation	Enable/Disable the phase compensation during baseband signal up-conversion according to 3GPP TS38.211, Chapter 5.4	True / False
Power Mode	Set the power mode on DUT	Target / Max / Min
Power Tx	Set the transmit power level on DUT, if Power Mode is set to 'Target'	
RSCMSA ACLR file	Specify the filename that contains limit line definition of ACLR (.xml file extension). The file is generated by VSE or FSW and should be made available on CMP200	None (Default) aclr_nr5g_100mhz1adj.xml
RSCMSA M5g file	Specify the filename that contains 5G NR signal configuration (.m5g file extension) defining frequency range, direction, test model, bandwidth and subcarrier spacing etc. The file is generated by VSE or FSW and should be made available on CMP200	None (Default)
RSCMSA SEM file	Specify the filename that contains limit line and ranges definition of SEM (.xml file extension) The file is generated by VSE or FSW and should be made available on CMP200	None (Default)
Subcarrier Spacing	Subcarrier spacing	
Test Model	Selection of test models defined by 3GPP 38.141-2	

Table 2-1 Parameter settings for small cell testing in WMT test editor

- 3. Launch VSE software on control PC unless signal analysis in VSE is disabled in test configuration 'testconfig.ini' file. Default setting will enable the VSE for signal analysis (details see Figure 2-24).
- 4. Launch WMT Test Runner GUI and follow the steps shown in Figure 2-23 to run the test plan.

🚸 RS 🔒 iestRunner			 	×
Edit				
Settings 2 DUT 1 0 / 0 Failed	Settings	3 · ×		
Elapsed Time [ms]	Enabled	Edit Open New rent State	 	^
	etc/testconfig.ini			
	1 🔹 🗹 Scroll	Expand All     Save     Cancel		
		4		~
		Run	Abo	rt

#### Figure 2-23 Run test plan in WMT TestRunner

The Step 3 shown in Figure 2-23 contains following options:

Edit - Modify an existing configuration file, e.g. 'testconfig.ini'

Open - Open a configuration file from local file system

New - Create a completely new configuration file

An example test configuration file named 'testconfig.ini' where the most relevant settings are highlighted in Figure 2-24. Bear in mind that the FDC values entered in the configuration file are based on the OTA system calibration described conceptually in Chapter 1.2.2.



Figure 2-24 Example 'testconfig.ini' configuration file

5. After the test plan execution is terminated in WMT Test Runner, the 'Overview' page (Figure 2-25) summaries the status of the test execution.

RS-WMT TestRunn	er			-		>
dit						
Overview DUT 1						
DUT 1						
3	/ 3 Failed: 0					
Elapsed Time [ms]		Current State				٦
2447	TX Testing					
8286	TX Breakpoint					
15353	ltem complete.					
15353	NR_FR2_BS, B257, CH 2054999, TM2, 100 MHz					
15353	TX Testing					
23208	TX Breakpoint					
24996	ltem complete.					
24996	NR_FR2_BS, B257, CH 2054999, TM3.1, 100 MHz					
24996	TX Testing					
32786	ltem complete.					
			Run		Abort	

#### Figure 2-25 Overview of test run in WMT Test Runner

#### The measurement results are presented in 'DUT1' page (Figure 2-26).

🚸 RS-WMT TestRunner						-	- 🗆	$\times$
Edit								
Overview DUT 1								
3 / 3 Failed: 0	v Log							
	Result Name	Min	Max	Match	Result	Unit	Verdict	^
□ NR_FR2_BS, B257, CH 2054999, TM1.1, 100								
Sampling								
	Reliability	0	0		0		PASS	
	ReferenceLevel				34.74	dBm		
Modulation								
	SyncOK	1	1		1		PASS	
	EVMAII				3.14	%		
	Power				16.69	dBm		
	FrequencyError				-1201.01	Hz		
Aclr								
	NRNeg		-25.70		-36.35	dB	PASS	
								~
						Run	Abort	

#### Figure 2-26 Measurement results in WMT Test Runner

If necessary, activate 'View Log' checkbox (Figure 2-27) will give a debugging view of the communications between control PC, CMP200 and DUT.

🛞 RS-WMT	TestRunner
----------	------------

dit	
verview DUT 1	
3 / 3 Failed: 0 View Log	
022-12-05 22:10:10,465 DEBUG:127.0.0.1:5025 >> '*OPC?\n'	~
022-12-05 22:10:14,207 DEBUG:127.0.0.1:5025 << '1\n'	
022-12-05 22:10:14,207 DEBUG:127.0.0.1:5025 >> 'CONFigure:NR5G:DL:CC1:RFUC:STATe ON\n'	
022-12-05 22:10:14,207 DEBUG:127.0.0.1:5025 >> 'CONFigure:NR5G:DL:CC1:RFUC:FZERo:MODE MANual\n'	
022-12-05 22:10:14,208 DEBUG:127.0.0.1:5025 >> 'CONFigure:NR5G:DL:CC1:RFUC:FZERo:FREQuency 26550000000.0\n'	
022-12-05 22:10:14,208 DEBUG:127.0.0.1:5025 >> '*OPC?\n'	
022-12-05 22:10:14,302 DEBUG:127.0.0.1:5025 << '1\n'	
022-12-05 22:10:14,303 DEBUG:127.0.0.1:5025 >> 'SYST:ERR:ALL?\n'	
022-12-05 22:10:14,303 DEBUG:127.0.0.1:5025 << '0,"No error"\n'	
022-12-05 22:10:14,303 DEBUG:127.0.0.1:5025 >> 'INSTrument:SELect IQ\n'	
022-12-05 22:10:14,304 DEBUG:127.0.0.1:5025 >> '*OPC?\n'	
022-12-05 22:10:14,315 DEBUG:127.0.0.1:5025 << '1\n'	
022-12-05 22:10:14,315 DEBUG:127.0.0.1:5025 >> 'INSTrument:BLOCk:CHANnel:SETTings:SOURcel FILE\n'	
022-12-05 22:10:14,316 DEBUG:127.0.0.1:5025 >> 'INSTrument:BLOCk:CHANnel:SETTings:FILE1:IQW "C:\\nobackup\\mwc2022-smal	
-cell-main\\etc\\iqdata\\cmp_iq_capture_3.iqw",50000000.0,625000000.0,IQPair\n'	
022-12-05 22:10:14,316 DEBUG:127.0.0.1:5025 >> 'INSTrument:SELect NR5G\n'	
000 10 05 00.10.14 016 BEBEC.105 0 0 1.5005 55 140BC01-1	-
Run Abort	

#### Figure 2-27 Debugging logfile in WMT Test Runner

6. In case signal analysis in VSE is desired (as described above in Step 3), open the VSE window (already run in the background) will populate the 5G NR measurement results, like constellation diagram, EVM vs. Carrier, power spectrum etc (Figure 2-28).

🚸 R&S VSE						- 🗆	×
File Edit Input & Output Meas Setup	Trace Marker Limits Window Help						
📑 🗎 🕋 🚟 🖬 🎝 🗄	🗄 🗔 5g NR 🔹 🕨	🖒 🌒 🝼 None 🔻 🔼	<u>∧</u> ∧₀ ∕₀ ∨ ∨₀ ∨₀			A 139 8	
Measurement Group Setup Instrumer	nts IQ Analyzer 5	G NR					
New Group     Channel	Replace Channel Ref Level 0.00 dBm	Freq 0 Hz Mode Downlink,	100 MHz Capture Time	20.0 ms BWP/SS All			
E ✓ Group 1		Frame Count	1 of 1(1) Frame			Auto De	emod Once
	Inp: File						
IQ Analyzer	SG NR: 1 Capture Bu	ffer	O 1 Clrw	<ul> <li>5G NR: 3 EVM vs Carrier</li> </ul>	1 Ave	g 🔍 2 Min 🔍 3 N	Max
	Frame Start Offsete 3:85081	1237 ms					
	0 diam			15 %			
	and all all all all all all all all all al	en, alle delle delle delle delle delle d	in the star with and	10 %			
	-50 dBm-						
	, w v u h	N <mark>N N N N N</mark>		Failingto material and a demonstration of the present	นารามปีป <i>าร</i> สารประช <sup>44</sup> สีสาราชาน-ปังปริปัตร	whiteward	needlen waaren e
				an a	and the fight and a share and a second s	an Redensioner Alan Topologies	and an and and
	0.0 ms	2.0 ms/	20.0 ms	0 Hz 9	).5 MHz/		95.04 MHz
	<ul> <li>5G NR: 6 Constellation</li> <li>Points Measured - 658944</li> </ul>	on Diagram		<ul> <li>5G NR: 5 Power Spectrum</li> </ul>		010	llrw
				-60 dBm/Hz			5
				-80 dBn (Hz			4
				V			
				-100 dBm/Hz			
				-120 dBm/Hz			
		*******		0 Hz 12	2.29 MHz/	1	22.88 MHz
	<ul> <li>5G NR: 2 Result Sum</li> </ul>	mary Selected Fran	Erame Averaged	<ul> <li>5G NR: 4 Alloc ID vs Symbol X Carrier</li> </ul>			
	Frame Results Average	d Mean Limit	Max Min 🔺	PSS SSS PBCH	PTRS	CORESET CO	RESET DMRS
	EVM PDSCH QPSK (%)	18.50		PBCH DMRS PDSCH DMRS CSI-RS	PRS P	DSCH	Not Used
	EVM PDSCH 64QAM (%	3.14 9.00	3.14 3.14				
	EVM PDSCH 256QAM ( EVM PDSCH 1024QAM	%) 4.50 (%)					
	Results for Selection B	WP/SS All, Subframe All, Slot All					
	EVM All (%) EVM Phys Channel (%)	3.13	3.36 2.95 3.37 2.94				
Play CONT	Record     EVM Phys Signal (%)	3.09	3.79 2.50 -	- 61 - 121 - 211 - 10 - 391 - 481	- 57 - 661 - 75	- 841 - 931-	1021-
· ·		Sync Found		·	Ready	VISA	$\sim$

Figure 2-28 5G NR measurements in VSE (standard items)

For in-depth debugging, more 5G NR signal analysis results can be opened in VSE through the steps shown in Figure 2-29.

🚸 R&S VSE				- 🗆 X
File Edit Input & Output Meas Setup Trace Marker Limits Wir	ndow Help			
🚰 🔛 🕋 🔐 🚾 🖵 🗔 🔠 🦳 😽 🗾	Player 🕨 None	▲ ペ ペ ♥ �゚ �゚ ┣゚ ๗		1:1 3 8
Measurement Group Setup	Instruments 4			
	Measurement Group Setup			
New Group     Other Channel	New Window DQ Analyzer		/FE All	
Ker .	Channel Information	Capture Buffer	55 All	
⊡	Active Windows	1 of 🖾 EVM vs Carrier		
	Active Windows	EVM vs Symbol		Auto Demod Once
IQ Analyzer	3	EVM vs RB		
SG		T CIrw 🕺 Freq Error vs Symbol	er O 1	Avg • 2 Min • 3 Max
🕂 🔽 5G NR 🕨 🕒 🔍		WW Freq Error vs Subframe		
0 dBm		Channel Elaborat		
ALL MAL	while have when while delive laters while when while black			
		Constellation		
-Soupern		Allocation Summary	۵۰، ۲۰ مردوم ۲۰۰۰ ۲۰۰۰ میلومی است. ۱۹۹۰ ۲۰ مردوم ۲۰۰۰ ۲۰۰۰ ۲۰۰۰ ۲۰۰۰ ۲۰۰۰ ۲۰۰۰ ۲۰۰۰ ۲۰	- Marth Carton Courses May 11 Marshares
		Bitstream	and an a shiply a section of the section of a first section of a short section of the	antighter and managed at a second and
0.0 ms	2.0 ms/	🖽 Channel Decoder	9.5 MHz/	95.04 MHz
▼ ¥ 5G	NR: 6 Constellation Diagram	🖽 Result Summary	Power Spectrum	O 1 Clrw
Points Mea	sured : 658944	Marker Table		
		EVM vs Symb X Carrier		
		Power vs Symb X Carrier		
	****	Alloc ID vs Symb X Carrier		
		RS Phase		
		RS Phase Diff		
	****	Beamforming Summary		
			12.29 MHz/	122.88 MHz

Figure 2-29 Other 5G NR measurements in VSE

Follow the steps shown in Figure 2-30 will provide the additional access to the IQ analysis results.

🚸 R&S VSE	_					- 🗆 X
File Edit Input & Output Meas Setup	Trace Marker Limits	Window Help				
Measurement Group Setup	SG NR	Player  Instruments	None			
	1	<ul> <li>Measurement Group Setup</li> </ul>				
New Group     Channel	R Inel Ref	Channel Information	IQ Analyzer	Magnitude	s BWP/SS All	
■ Group 1	0	Active Win	Count 1	of Real/mass (//Q)	1	Auto Demod Once
T IO Analyzer	2 Inp: File	3		Phase vs Time		Acto Delloca Orice
	Fraine St	iG NR: 1 Capture Buffer en <mark>: Off</mark> s <mark>attis 2000, 123</mark> 7 <mark>ms.</mark>		Clrw Marker Table	M vs Carrier	1 Avg 2 Min 3 Max
🕀 🔽 5G NR	🕑 🔍 📕 🛛 🛛 🖬			Statistic CCDF		
	<mark>M<sup>®</sup>M</mark>			10 %		



Furthermore, the steps shown in Figure 2-31 will bring the user to access 5G NR ALCR and SEM measurements in VSE.



Figure 2-31 Access the 5G NR ACLR and SEM measurements in VSE

An example of ACLR measurement and its limit check in VSE is given in Figure 2-32.

🚸 R&S VSE							- 🗆 🗙
File Edit Input & Output Meas Setup Trace Marker	r Limits Window Help						
🚰 🔛 🗃 🔛 🚾 🖵 🎝 🖽 🗔 5g nr	-	🖒 🌒 🝼 Non	e • 👗 🔏 🔏 🕅	× % % % & 📟	🚟 🗰 🗳 🛛		
Measurement Group Setup Instruments	IO Analyzer 5G N	2					
New Group     Channel     P     Replace Channel	el Ref Level 0.00 dBm						
🖻 🗹 Group 1 🔹 ト 🖒 🔍 🗙							
	Inp: File						
🗉 🗹 IQ Analyzer 🕨 👛 📃	5G NR: 1 Spectrum Emiss	ion Mask					• 1Rm Clrw
	Limit Check		P45	S	~		
🗄 🗹 SG NR 🛛 🕨 👛 📕							
	SEM_LIMIT_001_ABS_0						
	-40-d&m						~
	-60 dBm						
	-80 dBm						
	CF 26.55 GHz		1001 pts		14.0 MHz/		Span 140.0 MHz
	* 5G NR: 2 Result Summary						
	Sub Block A	Center	26.55 GHz	Tx Power Tx Bandwidth	15.45 dBm 95.040 MHz	RBW	1.000 MHz 5G NR DL
	Range Low	Range Up	RBW	Frequency	Power Abs	Power Rel	ΔLimit
	-70.000 MHz -60 500 MHz	-60.500 MHz -50 500 MHz	1.000 MHz 1.000 MHz	26.48821 GHz 26.49943 GHz	-39.88 dBm -10.40 dBm	-55.33 dB -25.85 dB	-19.88 dB -1.10 dB
	50.500 MHz	60.500 MHz	1.000 MHz	26.60057 GHz	-37.53 dBm	-52.98 dB	-28.23 dB
	60.500 MHz	70.000 MHz	1.000 MHz	26.61179 GHz	-41.96 dBm	-57.41 dB	-21.96 dB
Play CONT Record							

Figure 2-32 5G NR ACLR measurement with limit check in VSE

## 2.3 Summary

With WMT as a turn-key solution for FR2 small cell testing for production. It is easy and fast to run tests on a production floor with central controlling of CMP200, CMQ200 and DUT. Multiple DUT testing in parallel is possible which dramatically enhances the test efficiency for the production. Thanks to Python based R&S WMT, it can be easily integrated into existing customer automation framework. More debugging functionalities are supported by utilizing the external VSE software.

## **3 Test Solutions for R&D**

Figure 3-1 illustrates at high-level of the 5G base station architecture that is divided into two units, i.e. baseband (BB) unit and radio unit.



#### Figure 3-1 5G Base Station architecture

From small cell R&D testing perspective, it can be broken down into four main areas as illustrated in Figure 3-2. In this chapter, we will shed a light on major test solutions to cover some of the test requirements here.



Figure 3-2 Small cell testing aspects for R&D

## 3.1 Digital Design Test

In this chapter, a comprehensive view of the digital design tests with main focus in time-domain based on the oscilloscope and power rail probe is given. As illustrated in Figure 3-3, some testing aspects that we are going to tackle here involve:

- Signal Integrity (SI), including interface, PCB and interconnect test, clock tree, ADC/DAC verification
- Power Integrity (PI)
- Protocol level debug



#### Figure 3-3 A glance at digital design test in time domain and required test measurement instruments

Signal Integrity (SI) is the ability of a system to transfer signal without excessive loss, noise and distortion. It ensures that the transmitted signal can be accurately reconstructed at the receiver side. While Power integrity (PI) serves as an analysis to check the quality of DC power from the output of the DC converter to the ICs. SI and PI are of fundamental importance in digital design which are two distinct but related realms. They are crucial for the small cell digital design and driven by the increasing data rates and shrinking supply voltages as well as increasing integration densities that impose lower jitter and noise limits for signals and clocks and lead to tighter voltage variance.

The degradation of SI is usually caused by impedance mismatches, frequency response of the components and connectors (frequency dependent group delay / attenuation), crosstalk (caused by mutual inductance and/or mutual capacitance between conductors, e.g. near end crosstalk (NEXT) and far end crosstalk (FEXT) and noise caused by crosstalk effect, external noise, power variation and electromagnetic susceptibility (EMS) etc. on the printed circuit board (PCB) and interconnect parts (connectors and cables) of the design [5].

SI measurement utilizes typically the oscilloscope and vector network analyzer (VNA) where oscilloscope is mainly used to SI measurement in time domain with focus on jitter analysis (e.g. NEXT, FEXT) and eye diagram measurement, whereas VNA can be used for SI analysis in frequency domain, such as S-parameter and Time-domain Reflectometry (TDR) measurements.

In time-domain, following measurements need to be conducted on high speed interfaces,:

Eye diagram

It assesses the quality of the signal. The eye-closing indicates the degradation of the signal (a measurement example is shown in Figure 3-4).



Figure 3-4 An eye-closing eye diagram indicates the degradation of the signal transmission on the path

Jitter

Variation of the signal in time that leads to an incorrect decision at the receiver (RX). This will in turn create bit errors. Jitter analysis with jitter separation breaks down jitter and noise into individual components (bounded deterministic jitter DJ with sub-components and unbounded random jitter RJ) for characterization and debugging (see Figure 3-5).



Total Jitter Deterministic Jitter Random Jitter Data Dependent Jitter Periodic Jitter Other Bounded Uncorrelated Jitter Inter Symbol Interference Duty Cycle Distortion

Figure 3-5 Overview of jitter components



In Figure 3-6, it shows a snippet of the Jitter analysis on R&S Oscilloscope.

Figure 3-6 Jitter analysis on oscilloscope for fast debugging

The goal of PI is to ensure stable power rail voltages that are the basis for proper performance of any electronic design [6]. Excessive noise and ripple riding on the DC output should be suppressed. Low voltage levels of FPGAs and others digital components have to be resided in a strict tolerance window.

Plethora of challenges oppose to perform PI, mainly due to the increasing number of power rails in the design, demanding of fast load response, lower margins due to lower supply voltages and interfaces come alone with the dense design of mixed technologies. To overcome all these challenges and fulfill the measurement task, the test measurement equipment should possess following features:

- scope with fast update rate, fast FFT and low noise (R&S®RTP, R&S®RTO)
- specialized PI probe with bandwidth > 2GHz, adaptive impedance and extended offset range (R&S<sup>®</sup>RT-ZPR20, R&S<sup>®</sup>RT-ZPR40<sup>2</sup>) to achieve high measurement accuracy

Typical PI measurements as shown in Figure 3-7 include:

- Periodic and random disturbances (PARD)
- Drift
- Load response

<sup>&</sup>lt;sup>2</sup> Power rail probe R&S<sup>®</sup>RT-ZPR20 with 2 GHz probe bandwidth, R&S<sup>®</sup>RT-ZPR40 with 4 GHz probe bandwidth



#### Figure 3-7 Typical PI measurements

Figure 3-8 presents exemplarily power rail measurement on R&S<sup>®</sup>RTP by using a 2 GHz probe bandwidth power rail probe R&S<sup>®</sup>RT-ZPR20.



Figure 3-8 Power rail measurement using power rail probe R&S®RT-ZPR20 on R&S®RTP

Further reading in [6] and [7] to get more information on topic PI.

Multi-domain analysis is a function on the oscilloscope that features the fast signal analysis from different aspects in a time and event correlated manner. This includes:

- ► Time domain analysis
- Frequency domain analysis
- Protocol analysis
- Power analysis
- Logic analysis

Figure 3-9 shows an example of multi-domain analysis on a wireless product conducted on R&S RTP.



Figure 3-9 An example of multi-domain analysis on R&S®RTP

Last but not least, it is necessary to verify I/O interfaces following different protocols. Typically, small cell includes Ethernet interface. Sometimes, USB or console interface may also be part of the small cell for maintenance and operation purpose. All these require protocol level debugging in addition to SI and PI. An example of USB3.1 protocol level testing on R&S<sup>®</sup> oscilloscope RTP is given in Figure 3-10.

					Trigger		Horizontal	Acquisition	Info	2022-08-30
				<u>er</u>	Ser.Pat 0	V Norm	2.5 µs/	40 GSa/s Sample	RT (2	07:43:48
				<u>a</u>	CIK 0	V Stop		1 Mpts	-	- ÷
	Diagram1: C1,SB1	× 11 mm mm 18mmm								·····à····à·····
THE OWNER	History	_ ×								
21.25 pt	Available acqs	5174	7.5 μ	10 Jul	12.5 µi	15 per		17.5 at	202 (44)	2255 Jul 2285 Jul
_	Current ann	5171					-			
-	Zoom1: C1,SB1 ×	-16 433 4								
								a 4		
Ide	Autorepeat	Play> 0	MM		K M	Υ <u>β</u> γ	M N	Mah A	M	A-1 1 1
d	HPSTART: 1	HPSTART: 2	HPSTART 3	HPSTART	4	voei		Route S	trina	
005	CUD		CHD	EDE				0000		
oon	Smr	SHP	SHP	E F F F						
an arose			Desc	rambled K/[	) symbols	TTTTT				
618		620	621	622		623		624	625	626
n		K27 7+	K27 7+	K23 7+	J	n4 n			00	n n
~ L	N.C. N			12011			<u>A HI V</u>	00.0		
V	A AM	τų (	∾r yv		4 V - 1					V I
20.25					16		10			8 55 m
10.35111	isque	in pro	onu	- Squi				1.41		
	USB 3.1 Decode Re	sults SB 1 🗙								_
	State Start	Frame type	info 🔺	Details frame G				9		
1 Ok	-1.24945 µs Idle		-	State	Type	Numeric	Formatted	- <sup>-</sup>		
2 0k	-762.75 ns Skip -758.75 ns Idle		-	1 Ok		[hex] FB	SHP			
4 0k	-53.95 ns Skip				HPSTART: 2	[hex] FB	510	-		
5 Ok	-49.925 ns Idle		-	3 OK	HPSTART: 3	[hex] FB [hex] F7	549			
6 Ok	-9,875 m AOX				Type	[hex] 04				
2 Ok	662.75 ns Skip				Route String	[hex] 00				
g Ok				7 Ok	Device Address	[hex] 02	02h			
-					200100	thek]01	AUX			
C1		_								
125 n	nV/ USB 3	3.1 SS								
ov	50.0									
16 GHz	RT2M160									

Figure 3-10 An example of USB3.1 interface verification on R&S®RTP

## 3.2 Power Amplifier Characterization

RF frontend contains the circuit, components between the digital baseband unit and antenna (see Figure 3-1). Components, such as RF filter, RF amplifier, Low-noise amplifier (LNA), and mixer are involved here. In this chapter, we are going to tackle the power amplifier (PA) characterization and optimization.

To increase the PA efficiency, digital pre-distortion (DPD) is a common approach to linearize the output signal of a PA, which is being operated in its non-linear operating range. High bandwidth and high carrier frequency operated in FR2 opposes a big challenge for the PA design. The memory effect impacts on the PA performance significantly.

With proper implementation of DPD, Effective Isotropic Radiated Power (EIRP) can be improved by up to 3dB. As a result, it allows 30% reduction in the number of antenna array elements, relative to an array without DPD, for the same target EIRP [8].

For PA DPD characterization and development, R&S facilitates the test solutions with following advantages:

- Fast and easy characterization of PA (raw EVM, ACLR, crest factor, gain compression etc.), including AM/AM and AM/PM traces
- Capability of applying direct DPD that compensates the memory and other effects.

 Modeling and computation of a polynomial model of PA. The resultant polynomial coefficients can be used directly in the DPD algorithm that speeds up the DPD algorithm development



Figure 3-11 A solution of PA characterization (signal generator R&S<sup>®</sup>SMW200A + signal analyzer R&S<sup>®</sup>FSW) and exemplary measurements

Figure 3-11 shows in the upper part a test solution based on signal generator R&S<sup>®</sup>SMW200A and signal analyzer R&S<sup>®</sup>FSW to characterize the PA design. The lower part of the graph presents some exemplary measurements. Further reading, please refer to [9] [10].

The measurement personality on R&S<sup>®</sup>FSW with product options out of FSW-K18 option family covers different use cases as can be seen in Table 3-1 below.

Use Case	Description	Product Option
Full characterization of PA	<ul> <li>Distortion analysis with digital modulated or CW signals and known reference for RFFEs without and with frequency conversion. It includes the basic table or polynomial DPD modeling among other functions:</li> <li>AM/AM, AM/PM, Gain Compression</li> <li>Channel Response in Magnitude and Phase, Group Delay Input and output crest factor</li> <li>Accurate Psat measurement on sample basis</li> <li>P1dB, P2dB, P3dB compression points as input and output power levels</li> <li>Raw EVM without or with equalization on sample basis with known reference, which is more accurate in compression or low SNR scenarios.</li> <li>Raw EVM vs Power on sample basis without a power sweep</li> <li>IQ averaging</li> <li>Equalizer with coefficients export</li> <li>AcLR</li> <li>etc.</li> </ul>	R&S <sup>®</sup> FSW-K18 R&S <sup>®</sup> FSW-K18F (Channel Response Magnitude / Channel Response Phase / Group Delay)
Benchmarking of PA linearization without modeling	Direct DPD is iterative and based on sample by sample comparison of measurement and reference signal. Direct DPD delivers an I/Q file containing a pre-distorted waveform that can be played back on a signal generator	R&S <sup>®</sup> FSW-K18 and K18D

Use Case	Description	Product Option
PA DPD modeling	Memory Polynomial DPD, an extension to direct DPD. A memory polynomial equation that transfers the reference signal to the pre-distorted waveform is derived with specified polynomial order and memory order. Hammerstein model is a simplified memory polynomial model operating in real-time manner. The pre-distortion is applied in real time on the signal generator as it plays the undistorted signal.	R&S <sup>®</sup> FSW-K18, K18D and K18M R&S <sup>®</sup> SMx-K541 and K544 (options on signal generator, required for Hammerstein model approach)
Table 3-1 Power Amplifier characterizati	on on R&S <sup>®</sup> FSW	

## 3.3 Beamforming and MIMO

Beamforming and MIMO are the key technologies that are deployed in 5G to increase energy efficiency and to support higher data rate. Due to the high path loss at high frequency range, beamforming technology is even essential for FR2. It enhances the antenna gain through bundling transmitted energy and directing it towards particular direction or dedicated user.

Linearity and stability of the phase and level are important to ensure stable and reliable beamforming. Therefore, phase and gain adjustment per path as well as amplification capabilities for the Tx and Rx channels of an integrated beamformer IC have to be performed. Full characterization and calibration require simultaneous assessment of all antenna ports. With a multiport vector network analyzer (VNA), this task can be easily mastered. Simultaneous measurements on up to 24 ports makes it possible to characterize a beamforming IC in full detail and see the cross-correlation effects while monitoring all antenna connections. A glimpse of the test solution is illustrated in Figure 3-12. Further read in [11] to get more details about the test solution using multiport VNA R&S<sup>®</sup>ZNBT.



Figure 3-12 Characterization of a beamformer IC using multiport VNA R&S<sup>®</sup>ZNBT

## 3.4 Antenna Measurements

Antenna in Package (AiP) is widely used technology in mmW application, e.g. in 5G FR2. The antenna module that adopts the AiP miniaturizes the antenna. The module will also have more antenna elements, which increases radiation gain and enables more economical and energy efficient solutions. An example of AiP is given in Figure 3-13. Since RF components are fully integrated into the self-contained module, conducted tests are no longer feasible. Requirements for module optimization and validations all point to OTA tests during system level testing, integration and production.



Figure 3-13 An example of phased array antenna and its 3D antenna radiation pattern

Beamforming in FR2 demands accurate, reliable and efficient solutions. Figure 3-14 shows an antenna system consists of test measurement instrument (R&S<sup>®</sup>ZNA, R&S<sup>®</sup>FSW, R&S<sup>®</sup>SMW200A), software R&S<sup>®</sup>AMS32 and OTA chamber, e.g. R&S<sup>®</sup>ATS1000. The system enables fast and accurate characterization of phased array antenna and AiP featuring

- Direct far-field measurements with a quite zone 5 cm in diameter
- ► Integrated high-precision conical cut positioner with a 0.03° angular resolution
- 5G antenna module test and passive (CW) antenna measurements from 18 GHz to 67 GHz. Further reading in [12]
  - Very fast measurement due to trigger functionality of positioner VNA
  - Measurement of classic antenna parameters: directivity, realized gain, efficiency, 2D and 3D radiation pattern, half power beam-width, EIRP or TRP of active devices.
  - With VNA, also NF/FF transformation is possible in R&S<sup>®</sup>AMS32 based on magnitude and phase data
- ► 5G transceiver module tests: passive and active measurements using modulated signals. Utilizing signal generator (R&S<sup>®</sup>SMW200A) and/or spectrum analyzer (R&S<sup>®</sup>FSW), additional parameters like EVM, ACLR, SEM, also RX sensitivity, TIS, EIS can be measured along with the classic antenna parameters.





On top of the antenna characterization, since radiation patterns of miniaturized antennas can be influenced by temperature, so thermal diagnosis is gaining traction, especially in AiP. The R&S®ATS1000 can be equipped with a temperature testing option (see Figure 3-15, OTA chamber with temperature bubble) to perform fast and precise 3D thermal measurements from -40 °C to +85 °C.





More details of this test solution, please refer to [13].

Alternatively, OTA chamber R&S®ATS800R can also be facilitated for the antenna measurement.

## 3.5 Conformance

FR2 small cell as an integrated unit needs to be tested at system level against 3GPP TS38.141-2 conformance test specification [14] under OTA environment requiring anechoic chambers and positioners etc.

Conformance tests in [14] consist of transmitter (Tx) tests (chapter 6), receiver (Rx) tests (chapter 7), and performance verification in presence of a fading channel and AWGN generators (chapter 8).

#### R&S<sup>®</sup>SMW200A



R&S<sup>®</sup>FSW

Frequency ran Quite Zone siz DUT weight

Shielding effectiveness

#### Figure 3-16 Instruments for small cell conformance (R&S®SMW200A and R&S®FSW)

Small cell FR2 Tx and Rx conformance testing utilizes the R&S<sup>®</sup>SMW200A (signal generator) and R&S<sup>®</sup>FSW (signal analyzer) as test and measurement instruments (see Figure 3-16) plus OTA chamber. There are multiple choices of an OTA chamber as shown in Figure 3-17 for small cell testing in R&D. DUT form factor (determines the required Quite Zone size) and its weight are the main parameters of the chamber selection.

	R&S®ATS1000	R&S <sup>®</sup> ATS800R	R&S®ATS1800C	R&S <sup>®</sup> CMQ200 R&S <sup>®</sup> CMQ500
	R&S®ATS1000	R&S®ATS800R	R&S®ATS1800C	R&S®CMQ200 / CMQ500
ge	18 to 87 GHz	20 to 50 GHz	6 to 90 GHz	20 to 77 GHz / 0.7 to 77GHz
e	~5 cm at 30GHz	Ø 20 cm	Ø 30 cm	~1 to 2 cm
	20 kg centered	Static centered - 5 kg	15 kg centered	1 to 1.5 kg (depending on DUT
		On positioner 2.5 kg		nolaer)

>90 dB

60 dB / 80dB



>50 dB

More information about the base station transmitter and receiver testing in OTA environment according to [14] can be found in the R&S application note GFM324 [15] and GFM325 [16], respectively. Unless otherwise stated, test requirements related to medium range BS or local area BS specified in [14] should be applied for small cell testing.

50 dB

Based on OTA measurement software R&S<sup>®</sup>AMS32 together with its R&S<sup>®</sup>AMS32-BST option extension, FR2 small cell conformance testcases in OTA environment in line with 3GPP TS38.141-2 [14] (Tx and Rx) are supported and can be executed in an automatic and sequential way. To know more about the actual test coverage, please refer to the latest R&S<sup>®</sup>AMS32 release note. In Figure 3-18, it shows the small cell conformance testcase support in R&S<sup>®</sup>AMS32 that features the testcase batch run to enhance the test execution efficiency in addition.

	MMS32 Base Station Testing Utility		×				
Fite     Feety Cable     Extent     Wordow 7       EXCEX Explore     Device List	DUT DUT lateration for forces DUT lateration for forces DUT lateration for DUT lateration DUT lateratio	DUT Type         General Colloquida           C 100 Type         C 100 Type           C 100 Type         Good Type           P with Coll         Fuldware Sites           P with Coll         Fuldware Sites           C methods         Fuldware Sites           C methods         Fuldware Sites           C method         Fuldware Sites           E dit Tel Description         Thit Tel Care Primatem           Thit Tel Care Primatem         Sites           Downed Birl         Toom Sites           S 25         With Hars Thit 1	Locate Attrees real Generator Test Case Secolor Ron Test Case Attree Case to Bobs F tote B	AddS3 Bac States Nating Milly - Back Literator Ter Care Vertical States States States States Vertical States States States States States Vertical States States States States States States Vertical States States States States States States States Vertical States Stat	n  Tanganey (Pol.   Bardealth / Mrs. (SC)  2100 100  2100 100  2100 400  2100 400  2100 400	Md         Data         Version           60         Rot at         Rot at           60         Rot at         Rot at           100         Rot at         Rot at           100         Rot at         Rot at	X BanBitch
	Repot IF Add DUT Information IF Add Hardware Setup IT Add detailed Test Case Parameters	RTF IT HTML I POF	C All Test Cases C Most recent result for each TC Create Cumulative Report				

Figure 3-18 Base Station Test Utility in R&S<sup>®</sup>AMS32 (R&S<sup>®</sup>AMS32-BST)

In compliance with the performance requirements for PUSCH specified by TS38.141-2 [14], the one-box solution features the integrated fading and AWGN functionality as well as the handling of HARQ feedback signal (see Figure 3-19 and Figure 3-20). This requires single R&S<sup>®</sup> SMW200A with option K145 (option for 5G NR Closed-Loop BS Test). More information about base station performance testing can be found in [17].



testing simple and easy to setup.





Figure 3-20 Fading and AWGN function block on R&S<sup>®</sup>SMW200A

## 4 Conclusion

A comprehensive guide of test solutions for small cell testing in FR2 is given in this document. As shown in Figure 4-1, R&S has the whole solution portfolio for a small cell vendor to design, develop, produce and certify the small cells. In a nutshell, one-shop for all T&M needs.



Figure 4-1 One-stop small cell T&M solutions from R&S

## **5** Literature

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## 6 Appendix

## A **RSCMSA** Installation

- 1. Download RSCMSA Windows installer from R&S's Gloris customer portal (<u>https://gloris.rohde-schwarz.com/</u>), e.g. setup\_rscmsa\_1.23.0.exe
- 2. Run the installer. In 'R&S Software Distributor' window, ensure the 'Remote Installation' option is checked and press on 'Next' button to proceed

🅎 R&S Software Distributor		-		×
ROHDE&SCHWARZ				
Welcome to install:				
rscmsa				
1.23.0				
		Remote I	nstallation	
© Rohde & Schwarz GmbH & Co. KG		Add othe current d	r setups fro irectory	m
< Back	Next >	Exit	Hel	╸

3. Select the rscmsa package and press on 'Next' button to proceed

🗞 R&S Software Distributor			-		×
Packages Please select your packag	es to install				
rscmsa (1.23.0) ■ ▼ R&S modulation and	spectrum ana	lysis on CMP20	0	Inf	o
				Optio	ns
	< Back	Next >	Exit	Hel	p

4. Select the target CMP200 (identified by the displayed serial number) and press on 'Install' button to start the installation

🚸 R&S Software Distributor			-		×
Device List Please select your target d	evices				
Device         ✓         Serie           Image: CMP200         100	al No.   Version 132 4 0 0 10	Satus renote connected	>	Scan verbos Rescar Report Options Device Grr DEFAULT	e n 
One R&S Device found.				Send Log	
	< Back	Install	Exit	Help	

5. Wait until the installation process is finished. This can be confirmed in the status (Ready: see report for ..)

🍫 R&S Software	Distrib	utor			-	
Device List Please selec	t your ta	arget devices				
Device	A.	Serial No.	Version	Status		Scan
CMP200		100132	4.0.0.10.	Ready: see	e report for deta	Rescan
						Report
						Options
						Device Group:
						DEFAULT
<					>	
One R&S Device fo	und.					Send Log
		<	Back	Install	Exit	Help

6. Click on the 'Report ...' button to launch the report window and the text '...was installed' indicates the successful installation



- 7. Close report window and exit from software distributer by clicking 'Exit'
- 8. Reboot the CMP200 to complete installation.

## Rohde & Schwarz

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